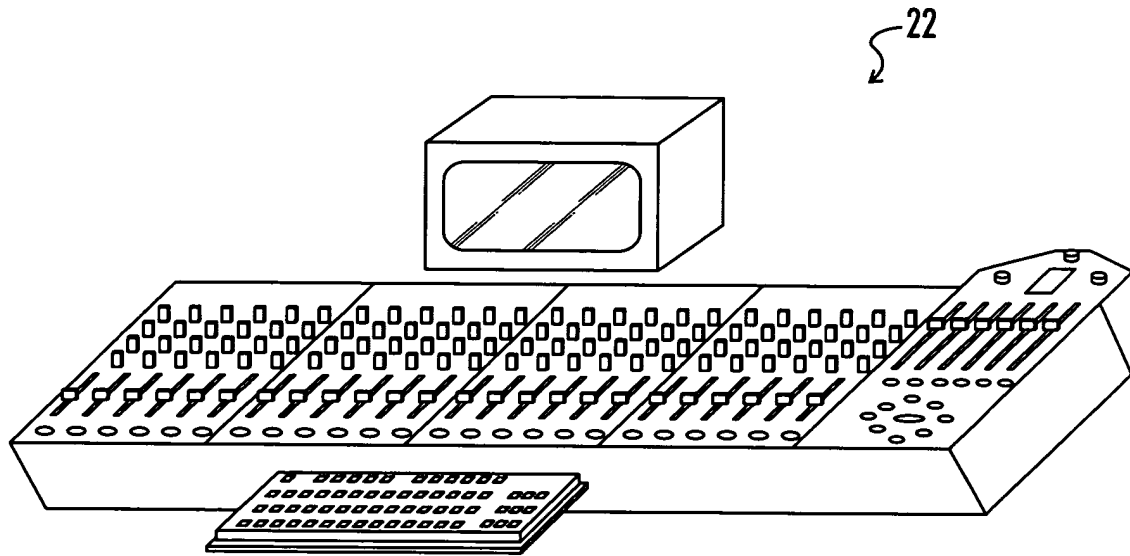
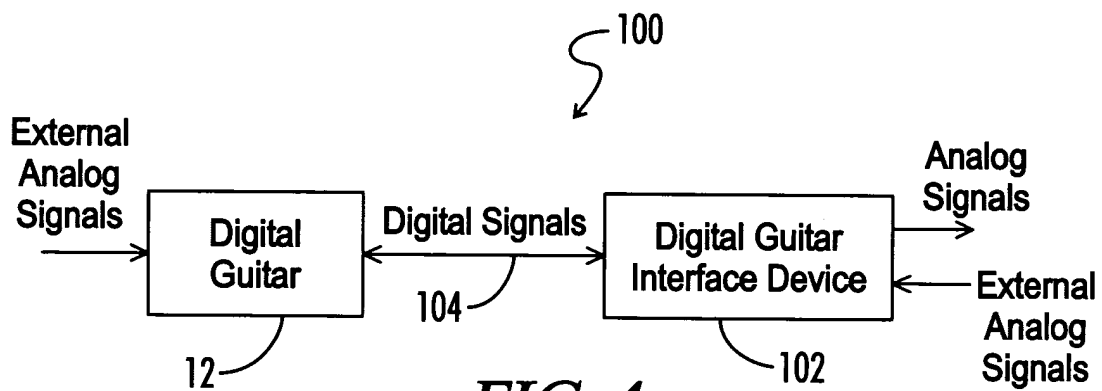


+



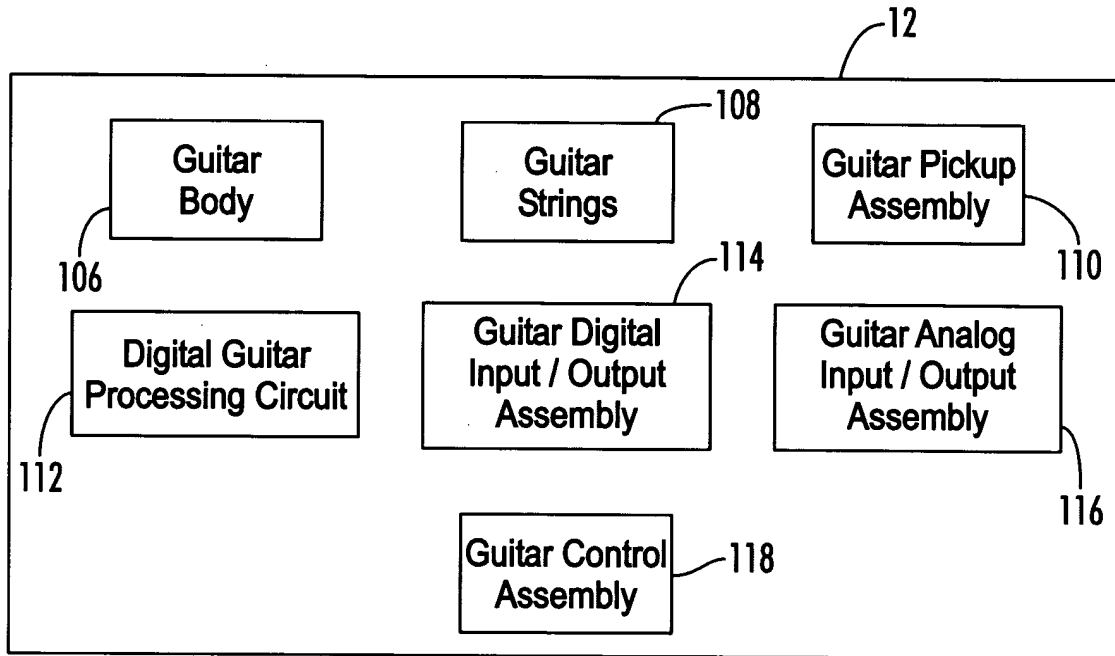
**FIG. 3**



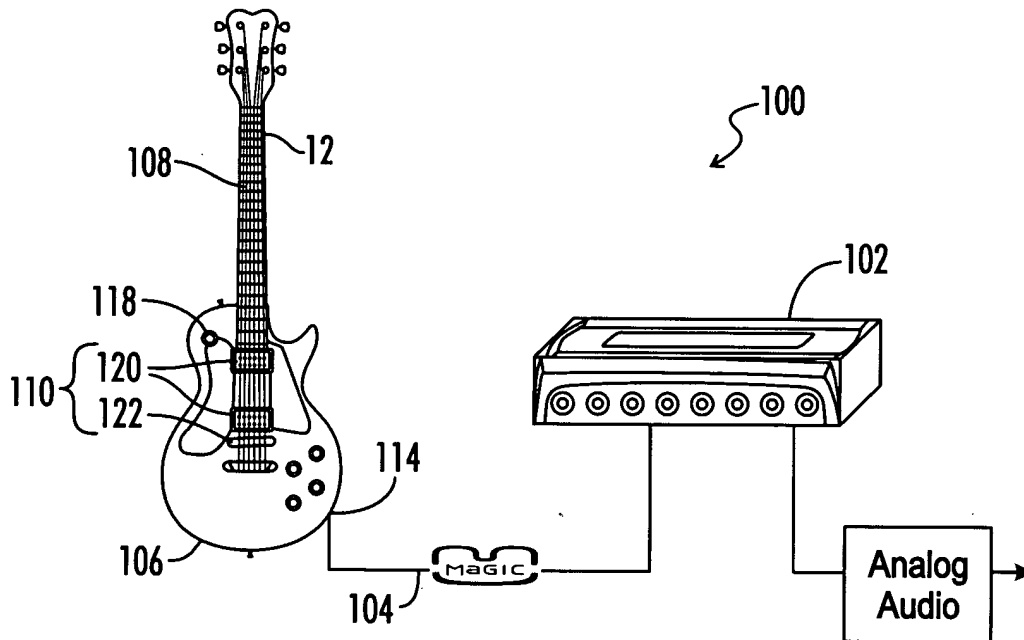
**FIG. 4**

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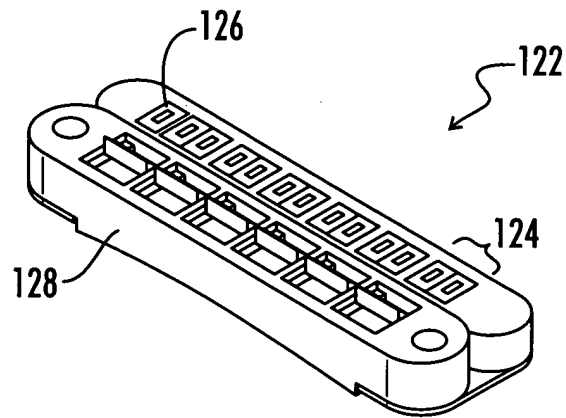


*FIG. 5*

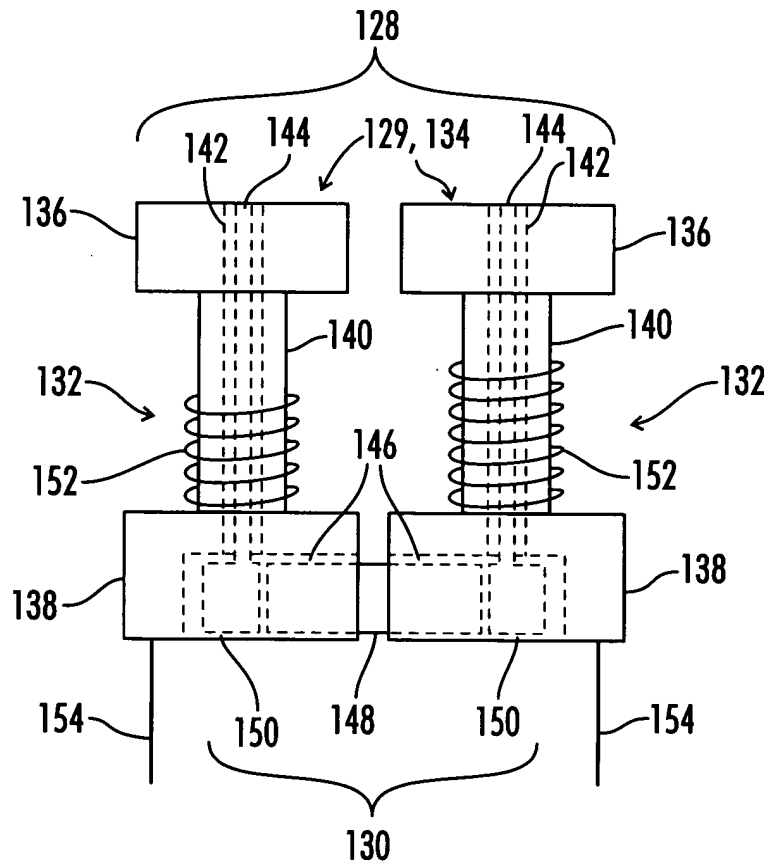


*FIG. 6*

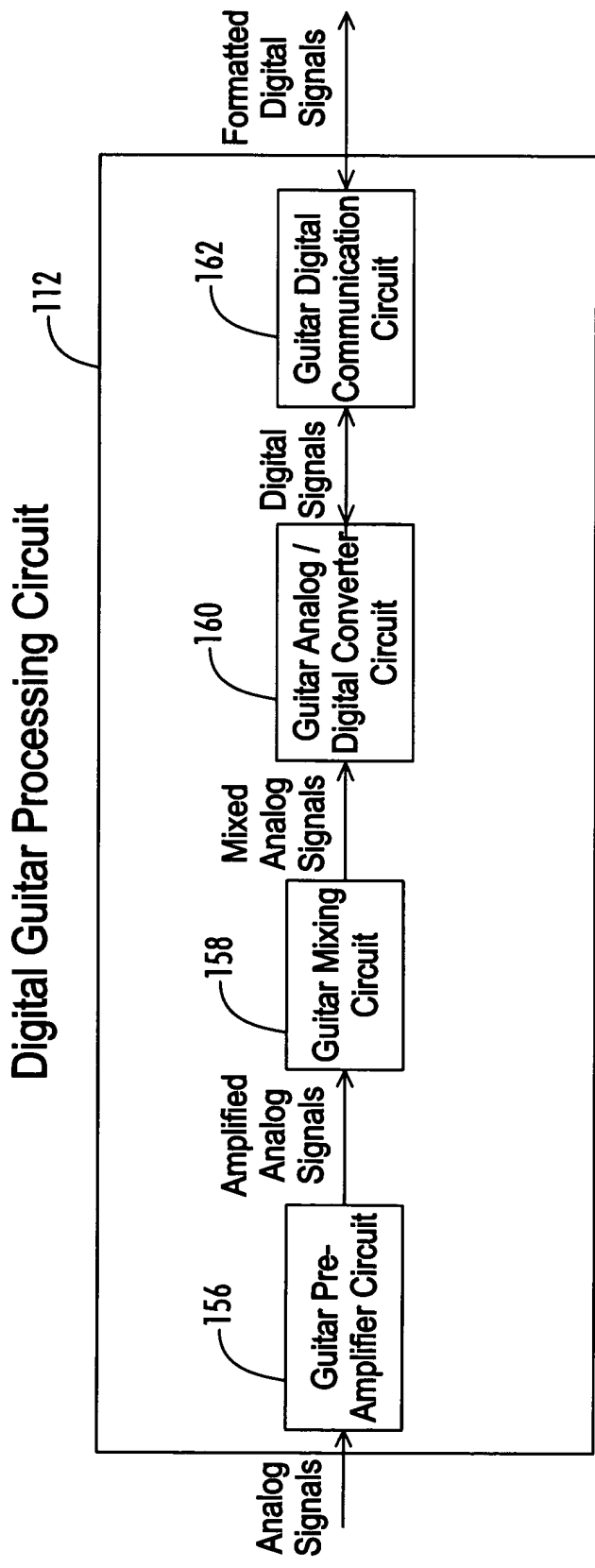
+



**FIG. 7**



**FIG. 8**



*FIG. 9*

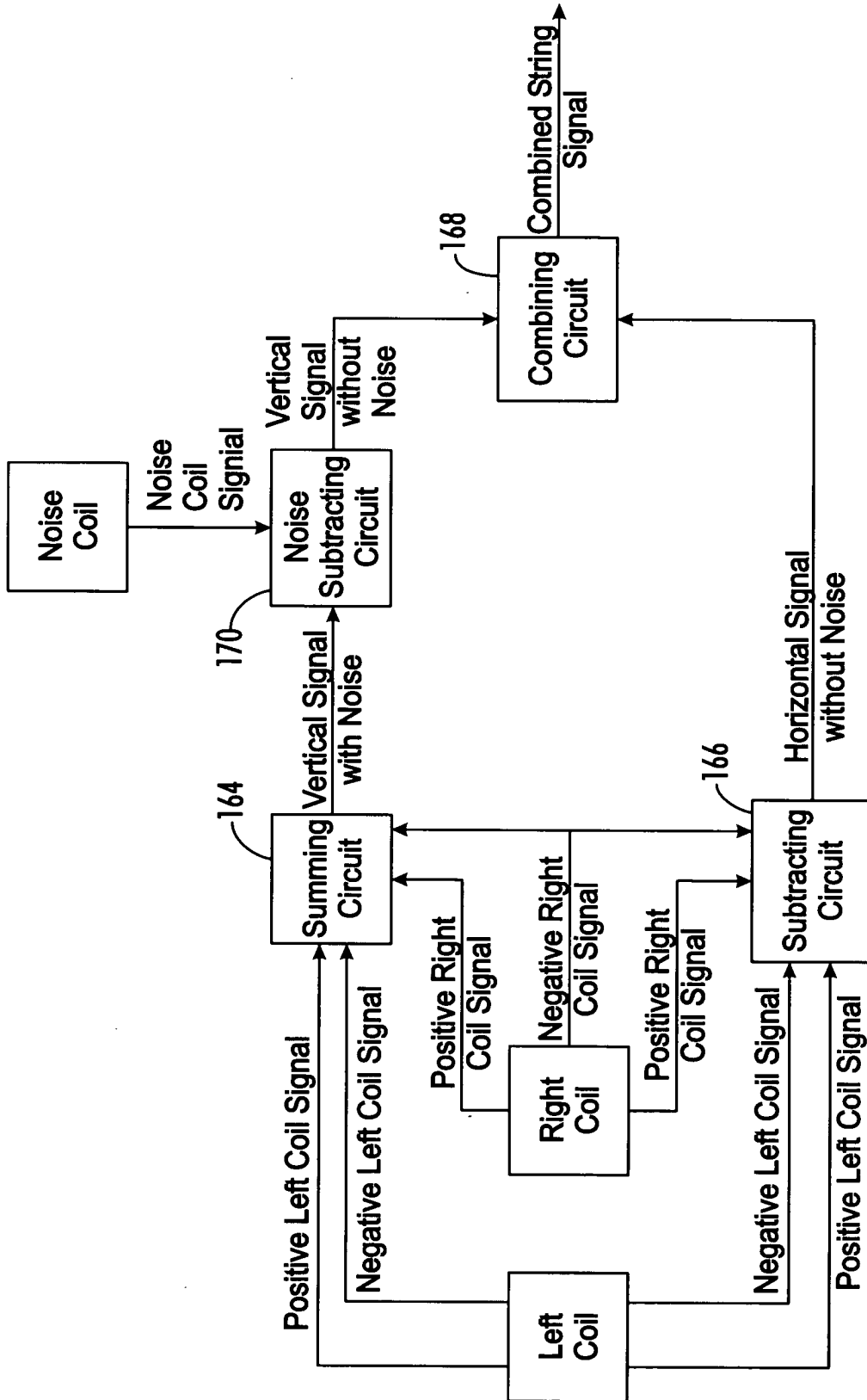


FIG. 10

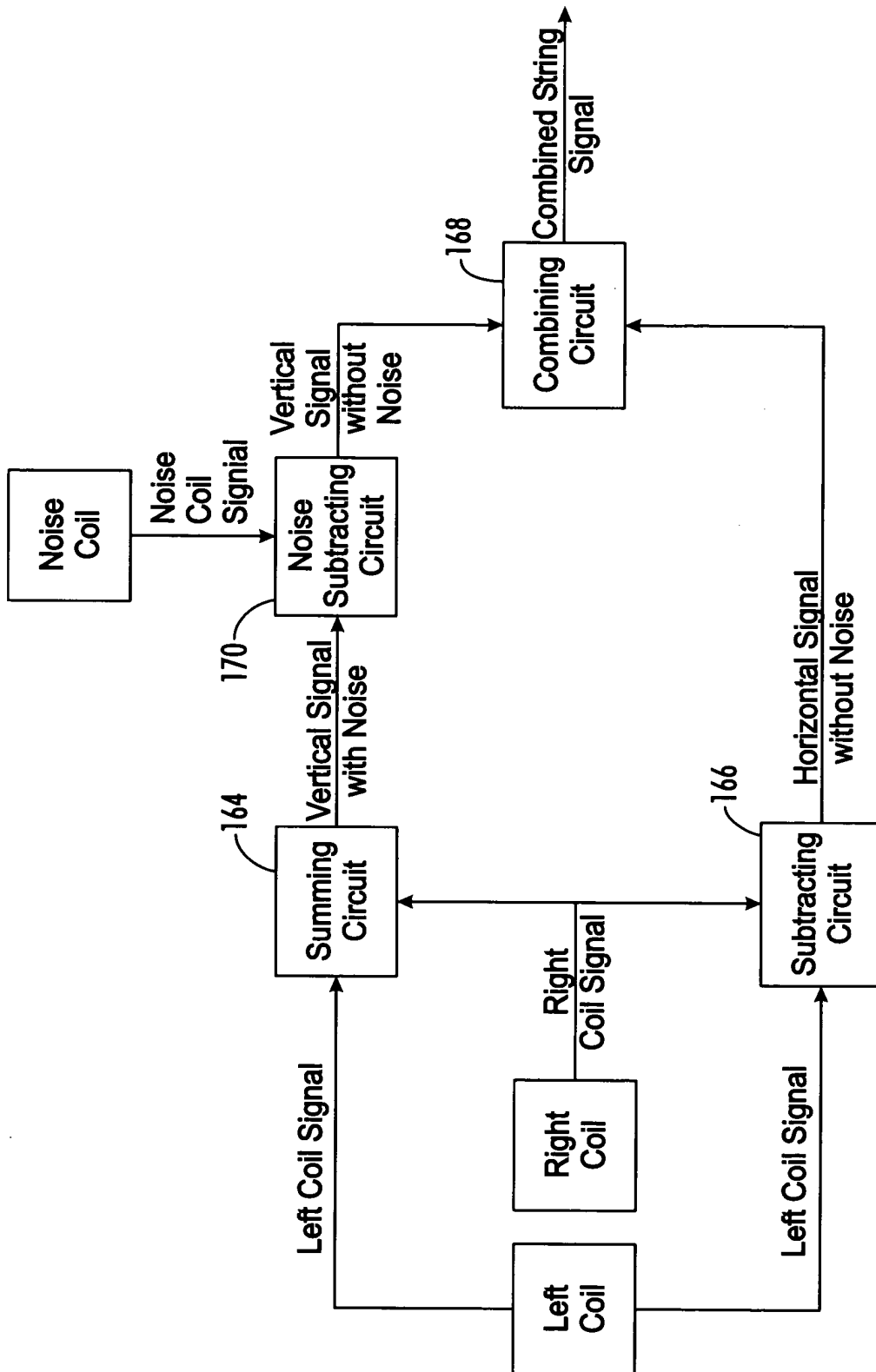
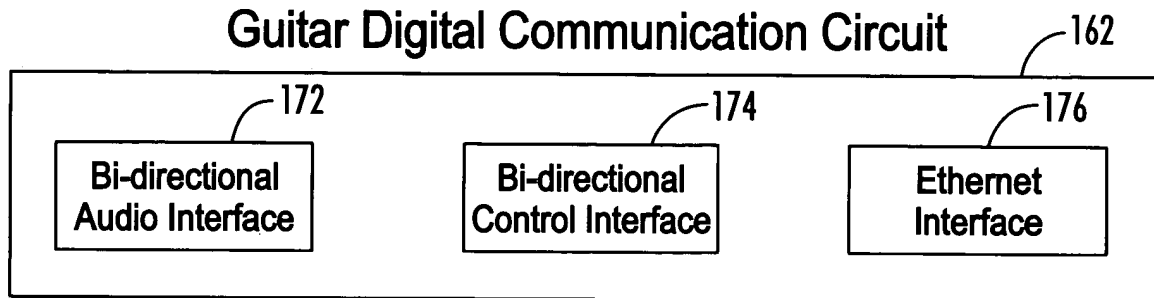
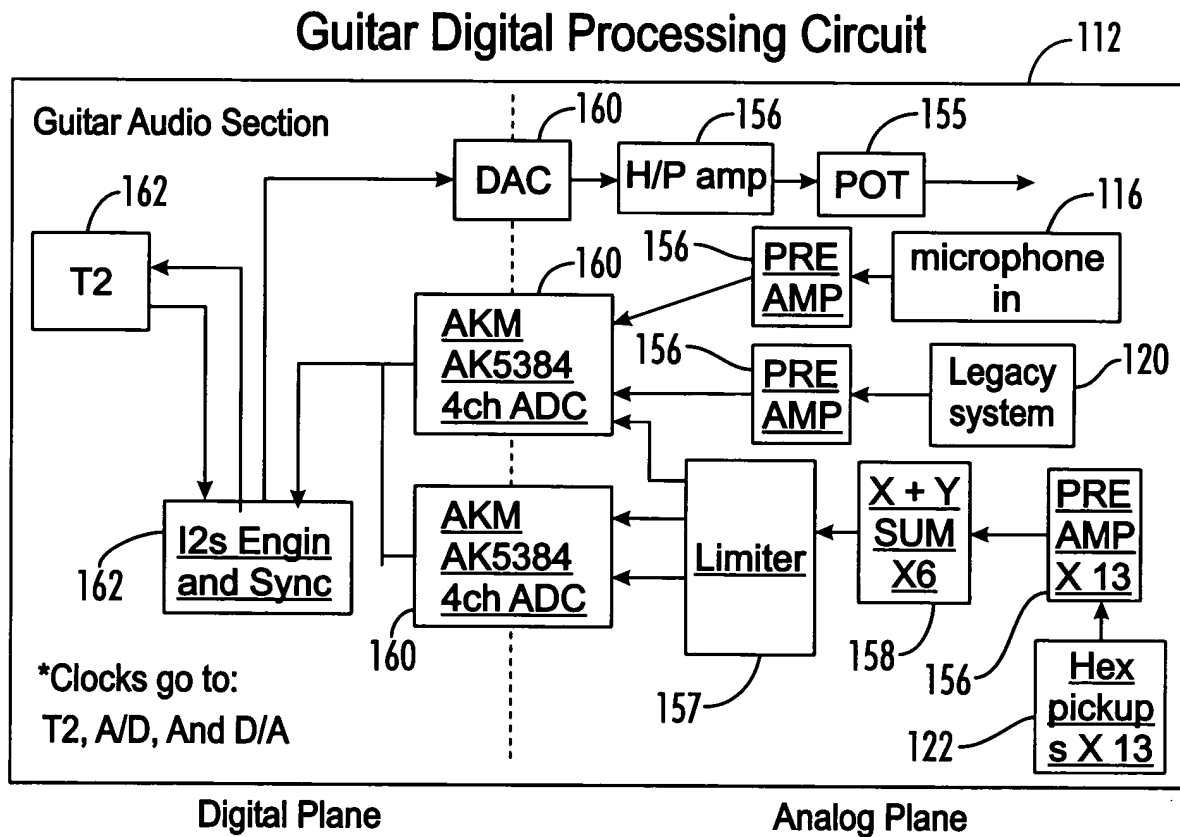


FIG. 11

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**FIG. 12**

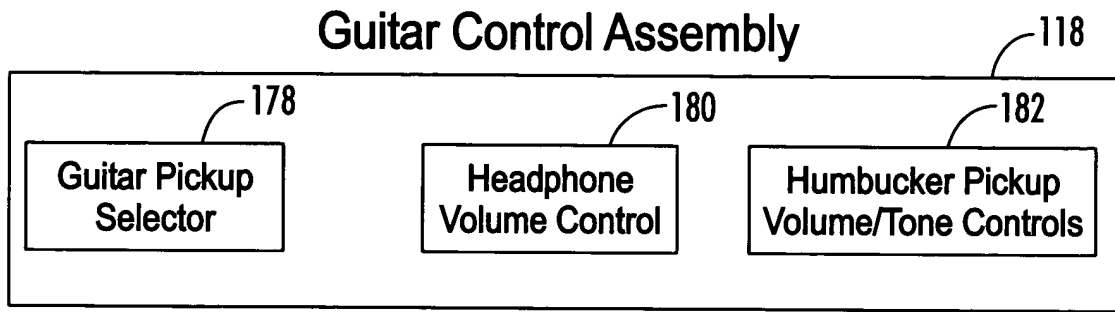


**FIG. 13**

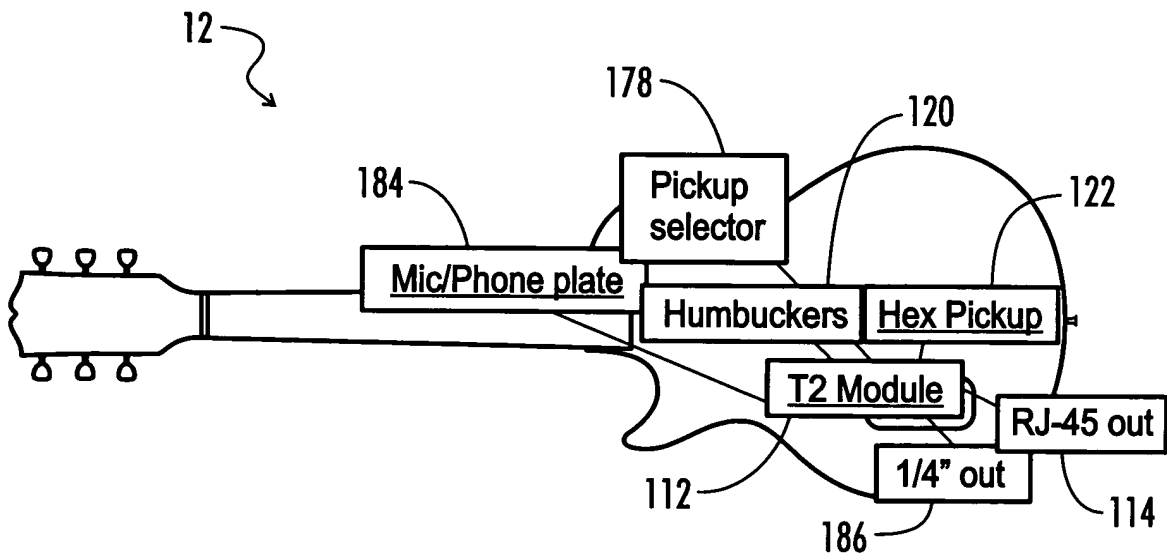
+



+



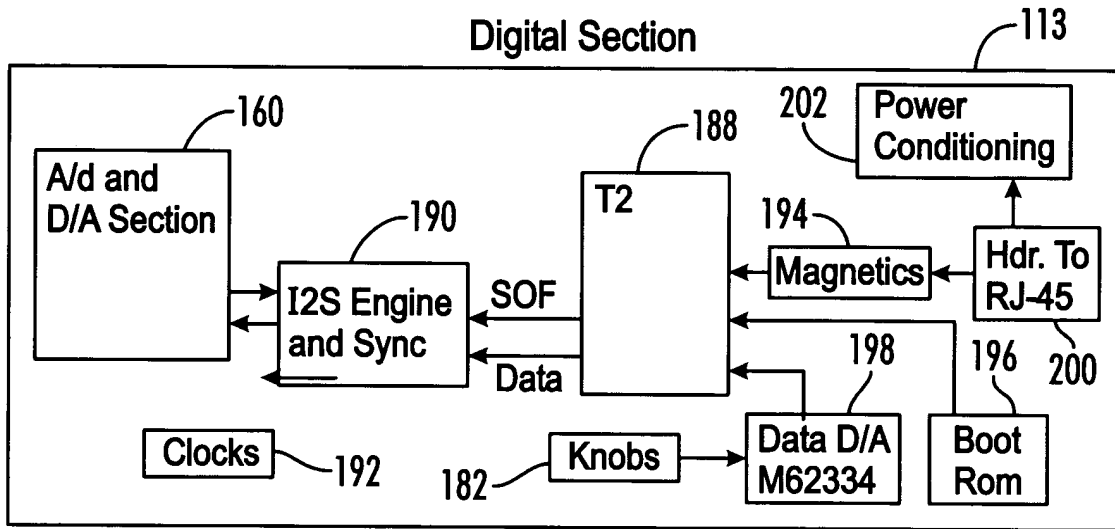
*FIG. 14*



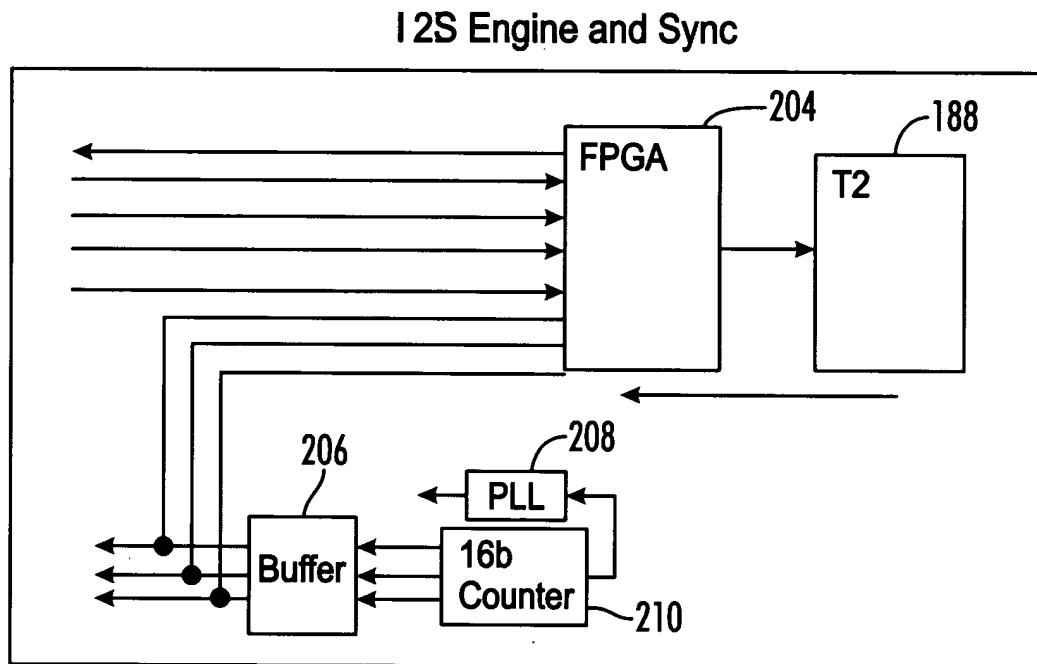
*FIG. 15*

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**FIG. 17**



**FIG. 18**

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Preamplifier Section

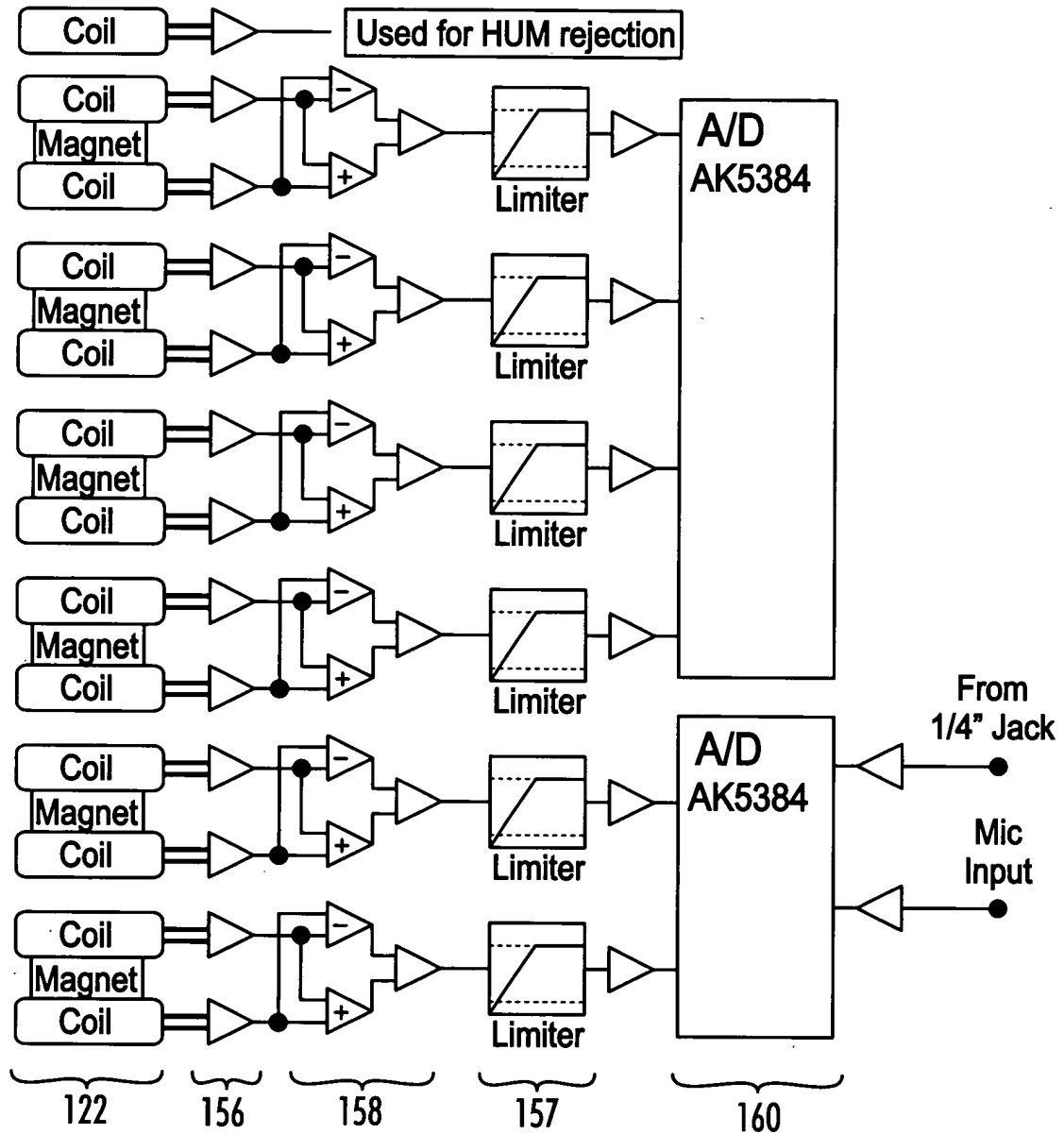
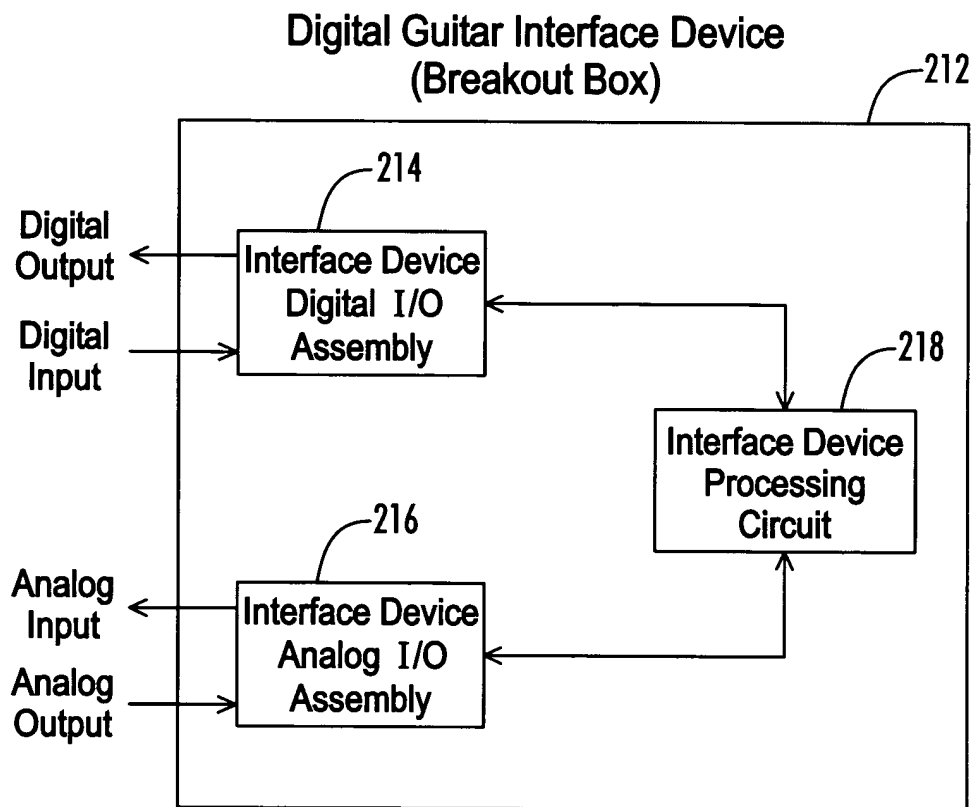
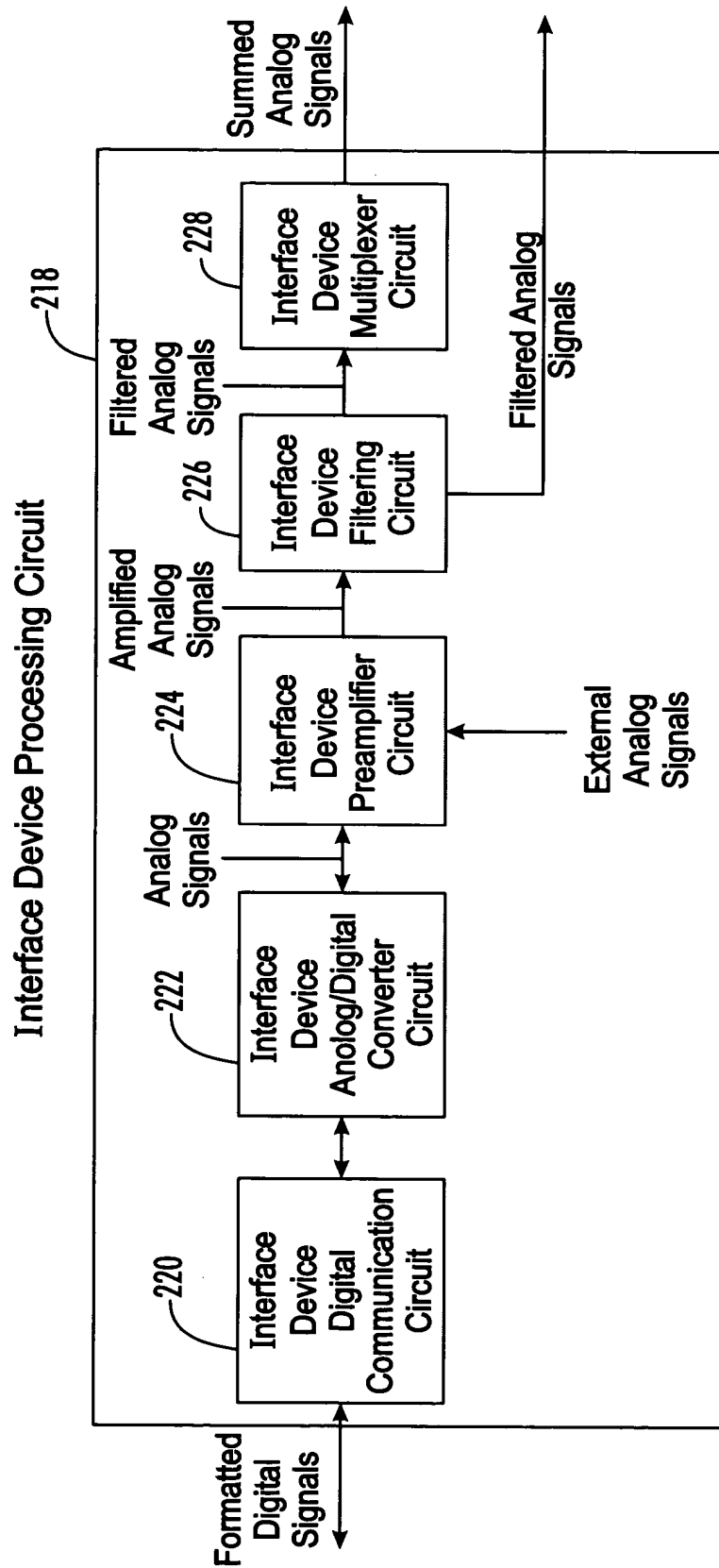


FIG. 16

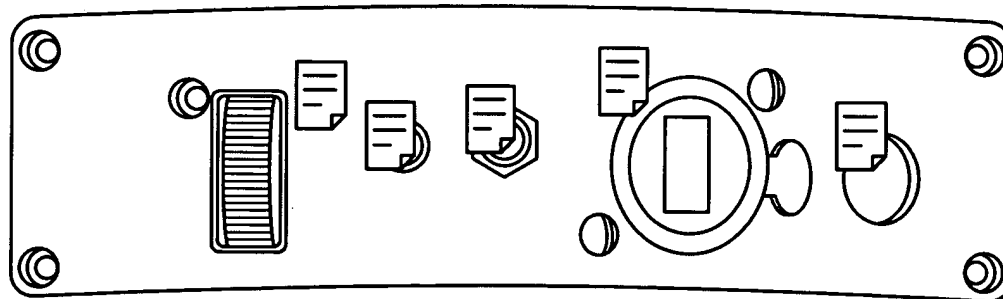
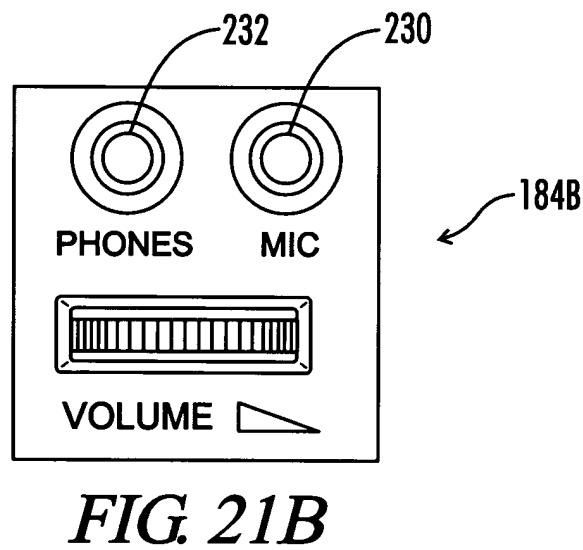
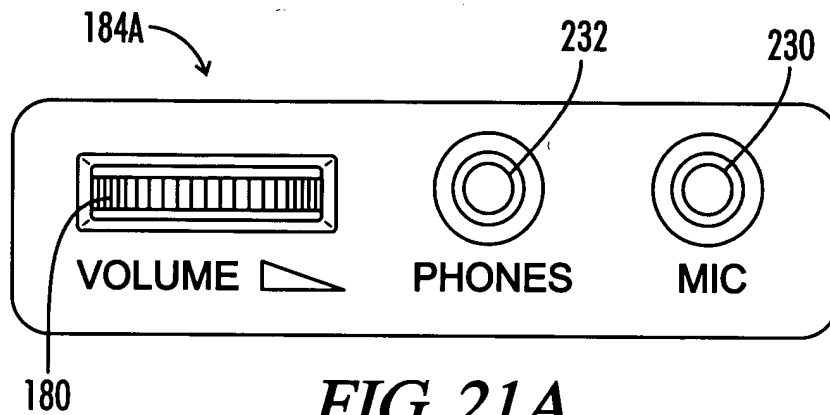


**FIG. 19**



**FIG. 20**

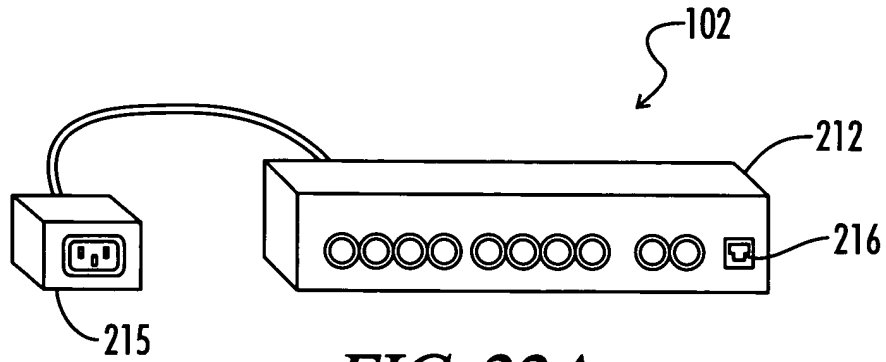
+



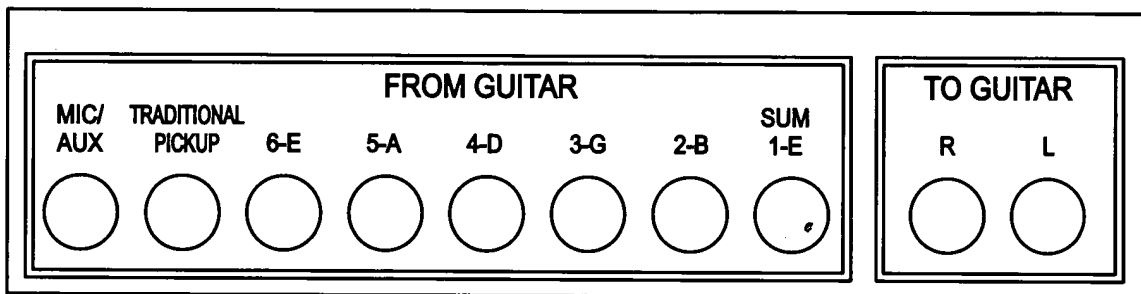
*FIG. 21C*

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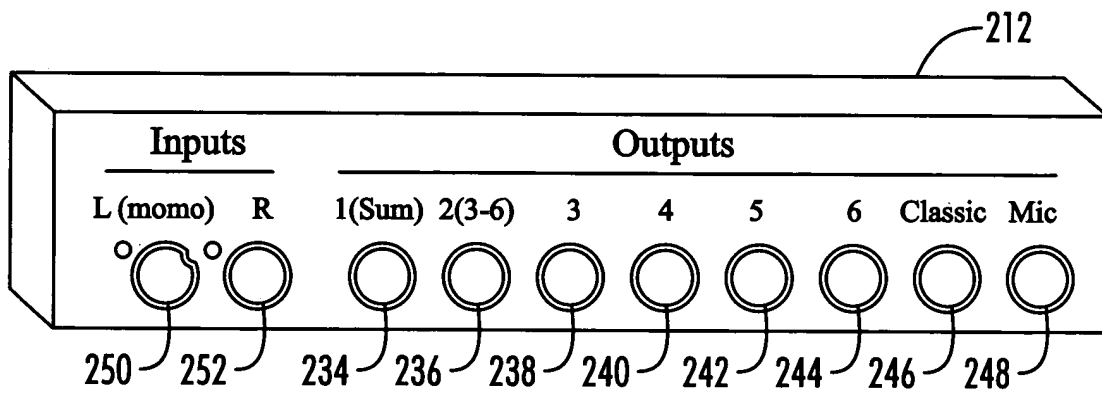
+



**FIG. 22A**



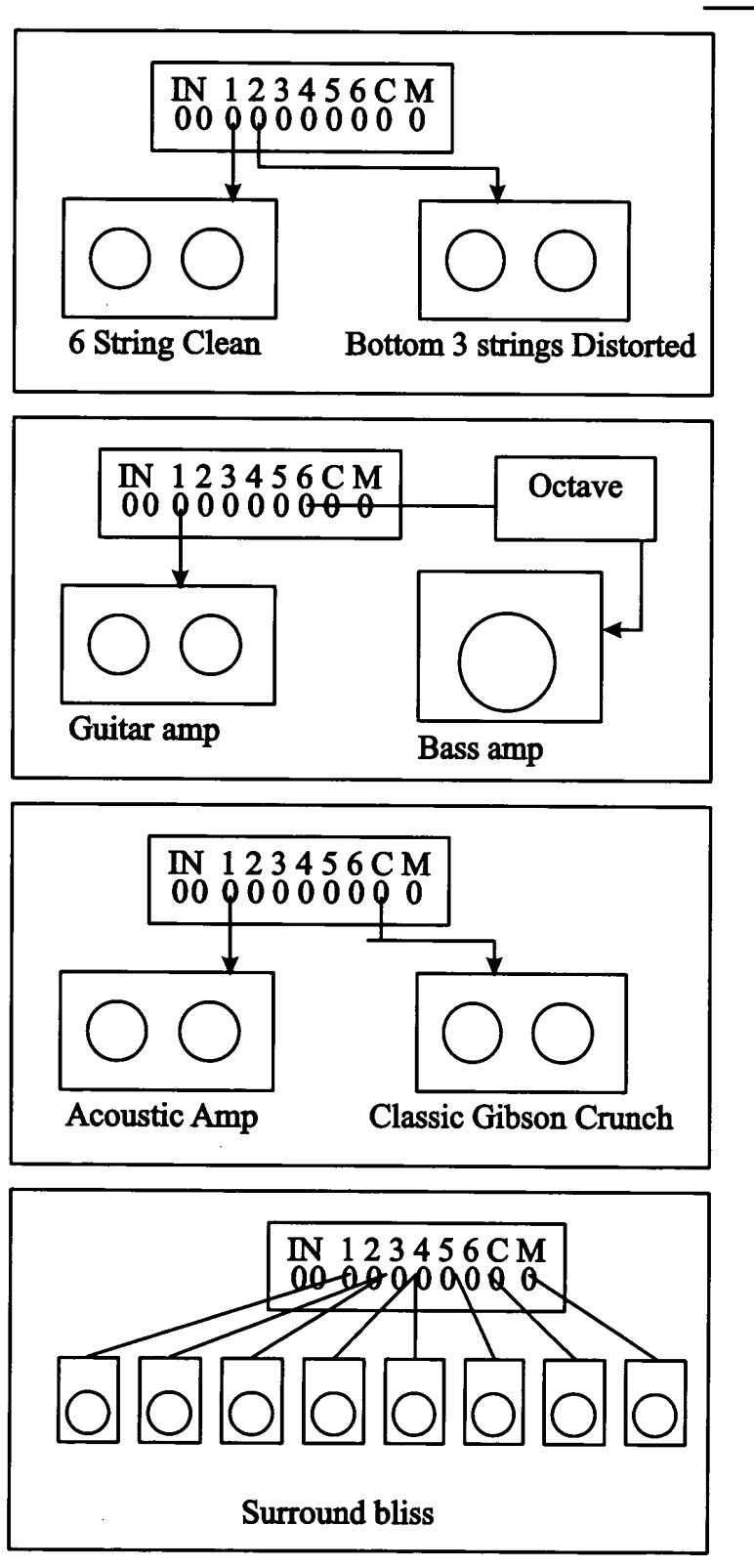
**FIG. 22B**



**FIG. 23**

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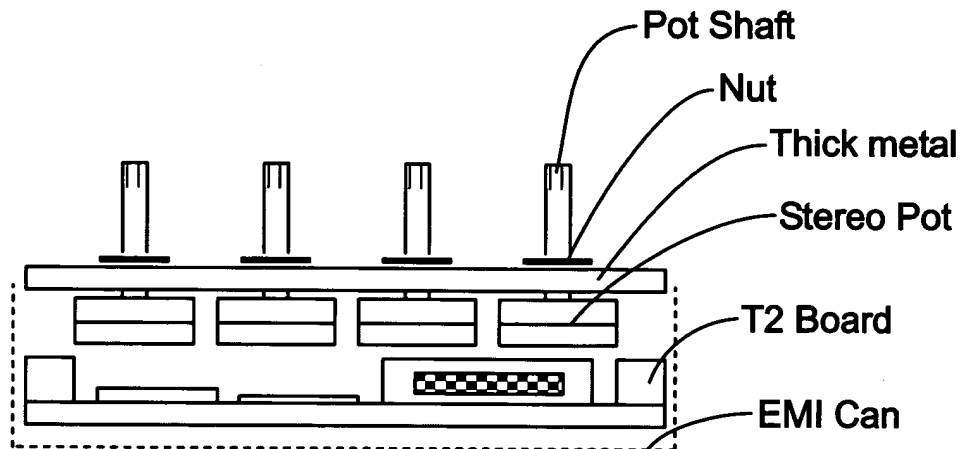
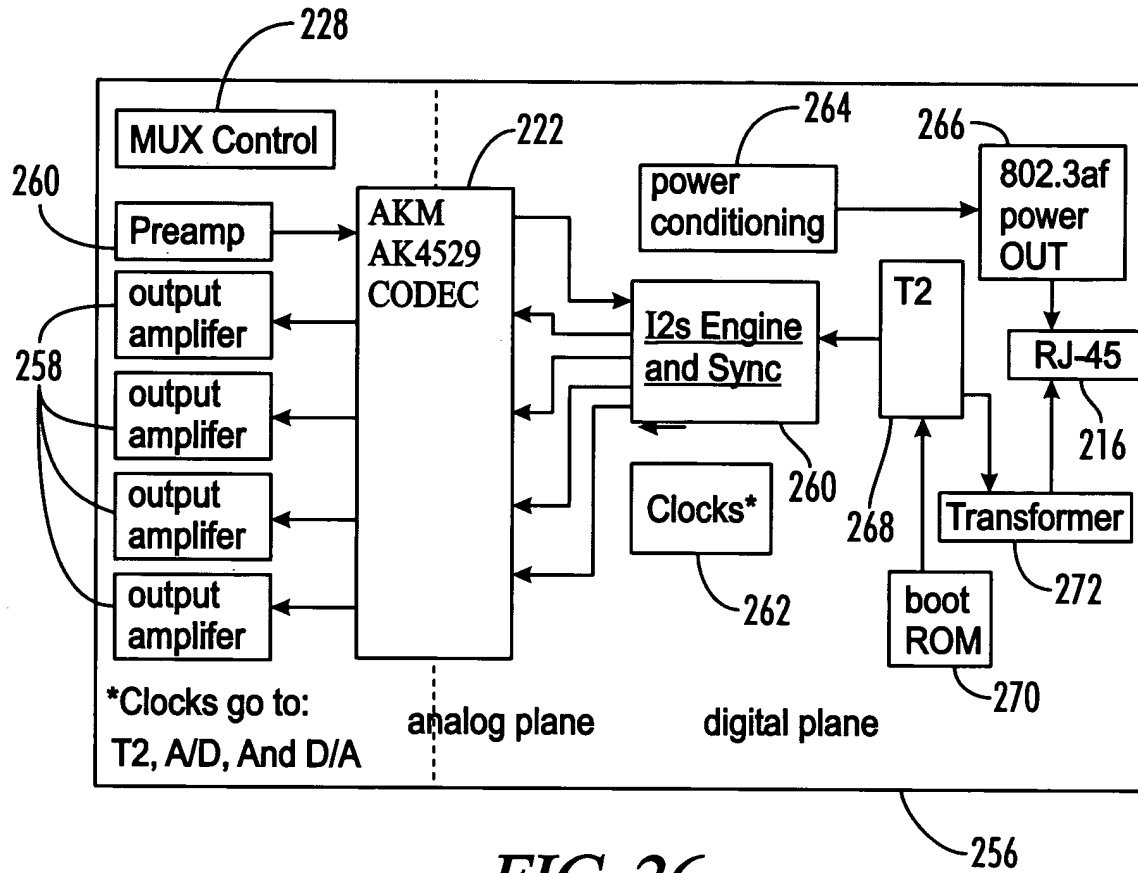
+





+

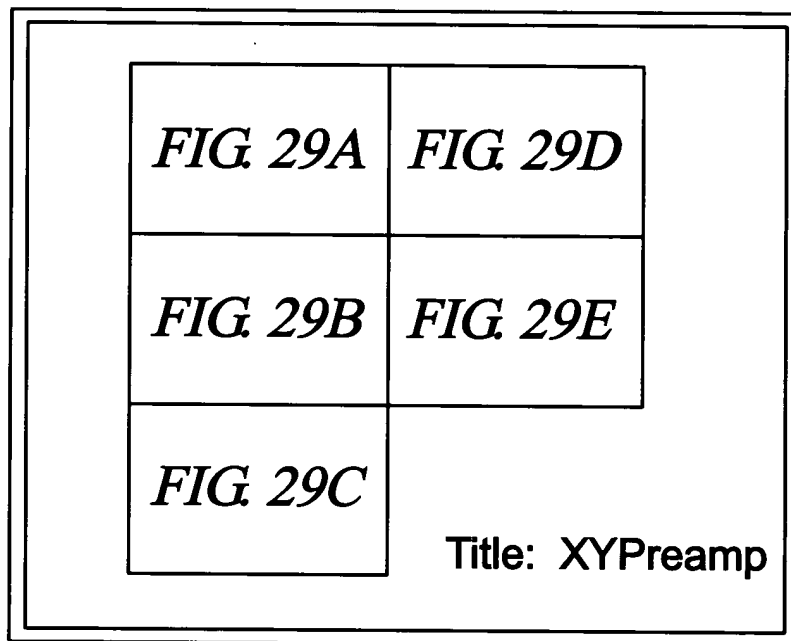
+



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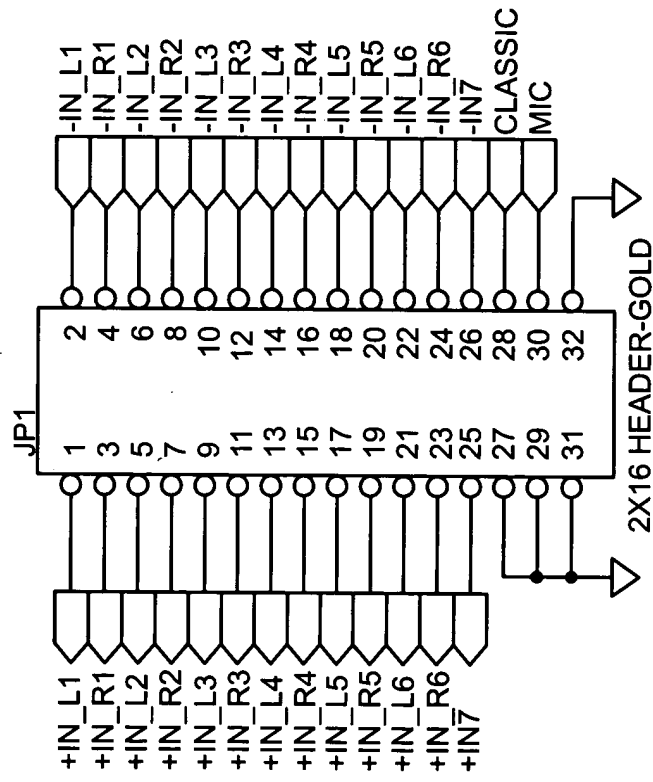
+



*FIG. 29*

+

Pickup & Mic Inputs  
Connector to be specified



HEX Pickup

- +IN\_L1, -IN\_L1, +IN\_R1, -IN\_R1 = High E string
- +IN\_L2, -IN\_L2, +IN\_R2, -IN\_R2 = B string
- +IN\_L3, -IN\_L3, +IN\_R3, -IN\_R3 = G string
- +IN\_L4, -IN\_L4, +IN\_R4, -IN\_R4 = D string
- +IN\_L5, -IN\_L5, +IN\_R5, -IN\_R5 = A string
- +IN\_L6, -IN\_L6, +IN\_R6, -IN\_R6 = Low E string
- +IN\_7, -IN\_7 = Hum cancellation coil

CLASSIC = Hum Bucker pickup (Classic is actually input from control board. The header connection will be an output to the 1/4" jack)

MIC = microphone input (this input has 12V phantom power for capacitor microphones)

FIG. 29A



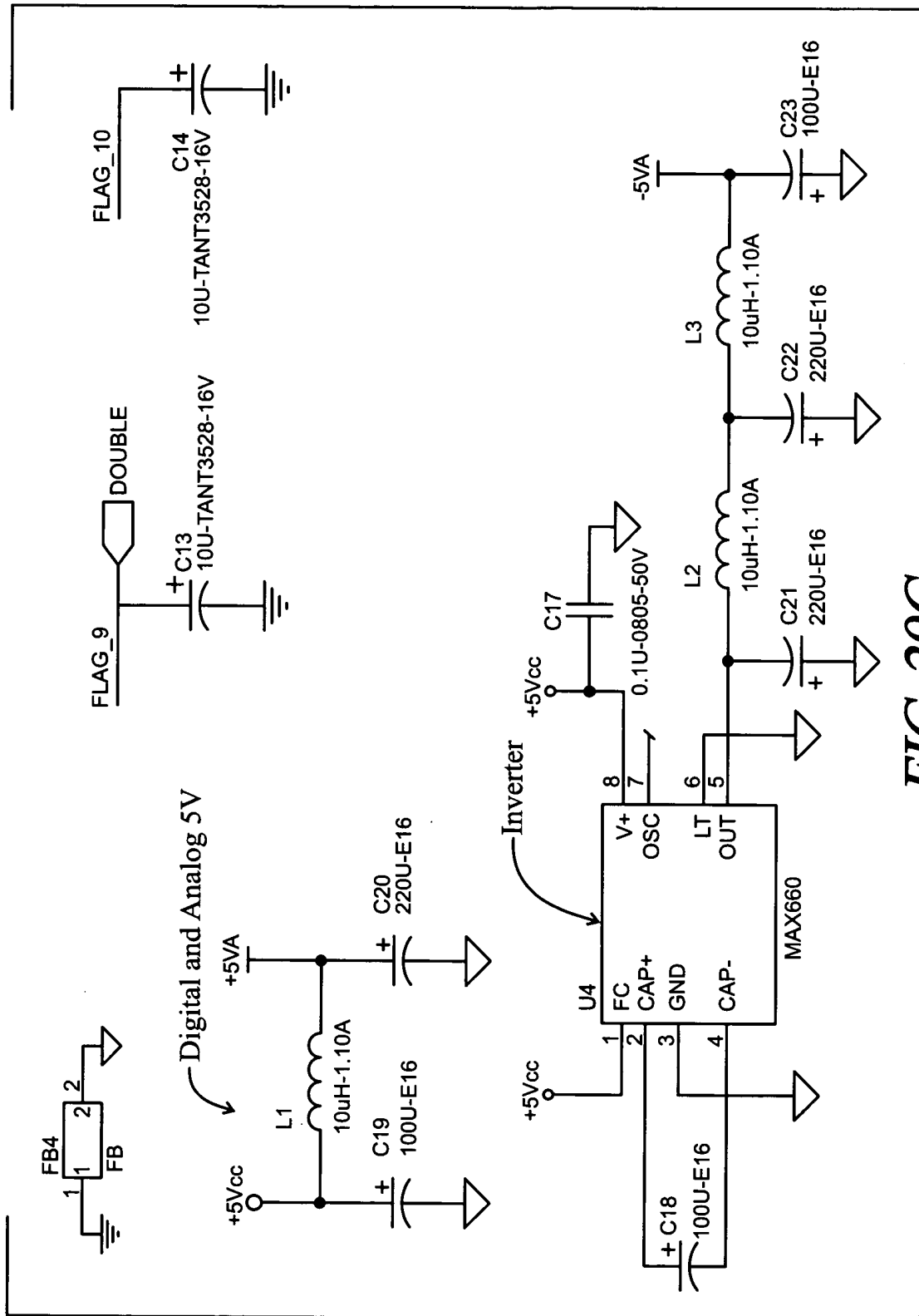
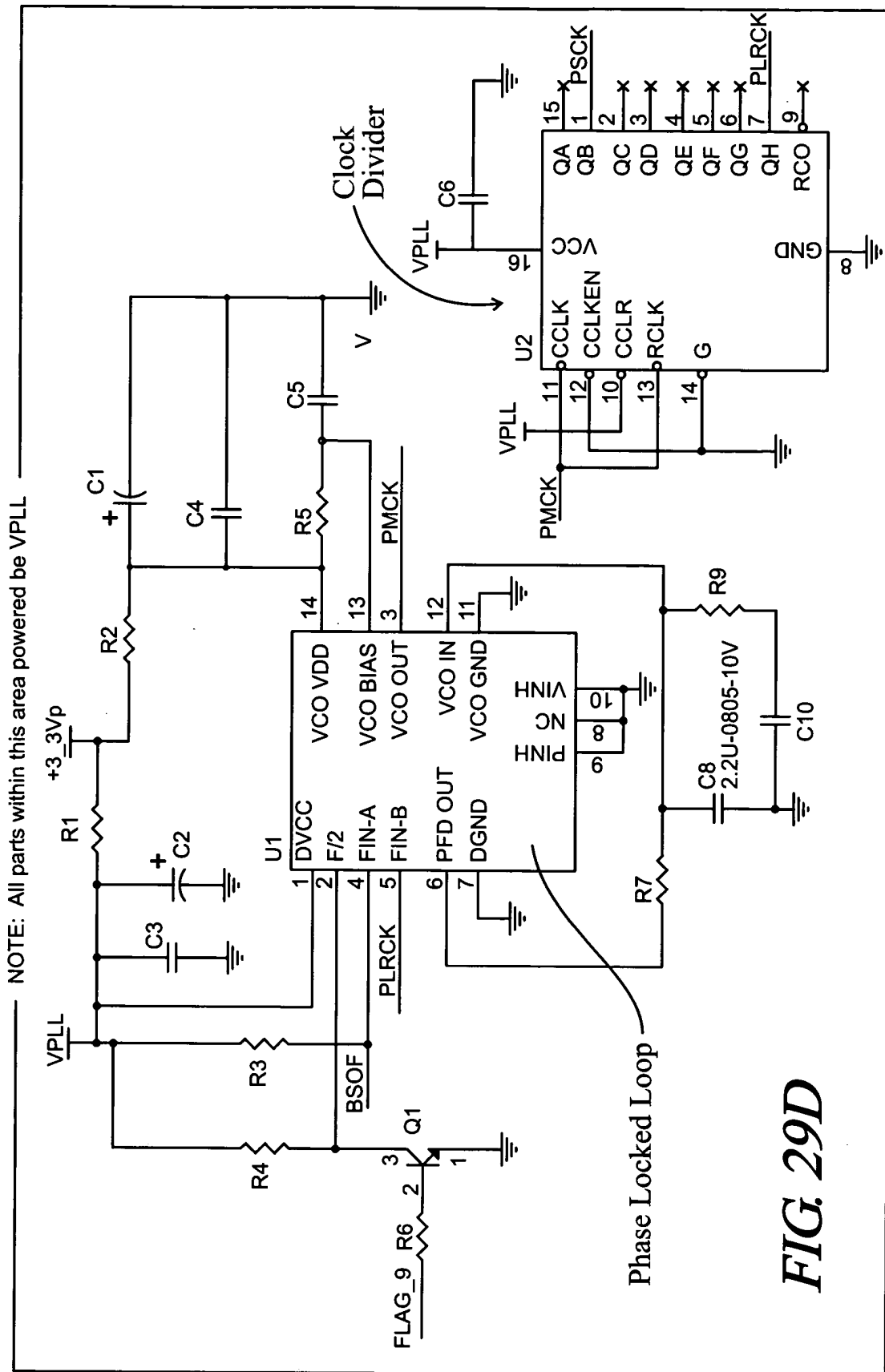


FIG. 29C



**FIG. 29D**



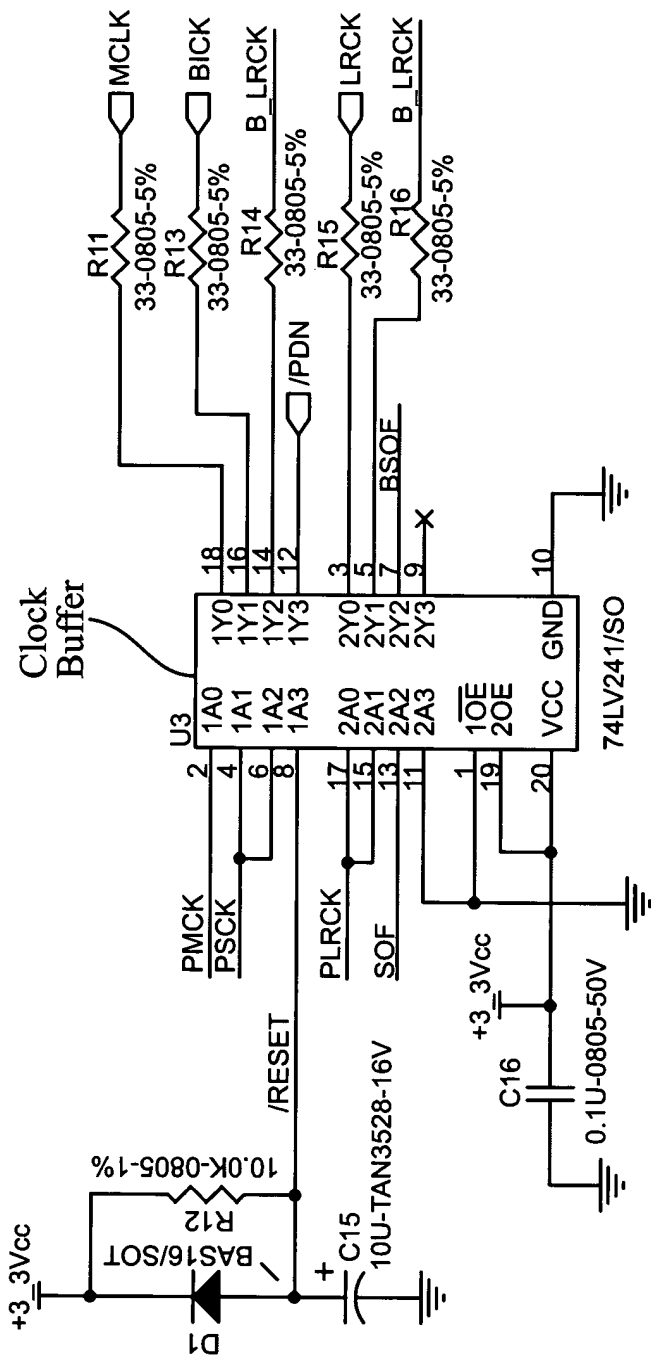
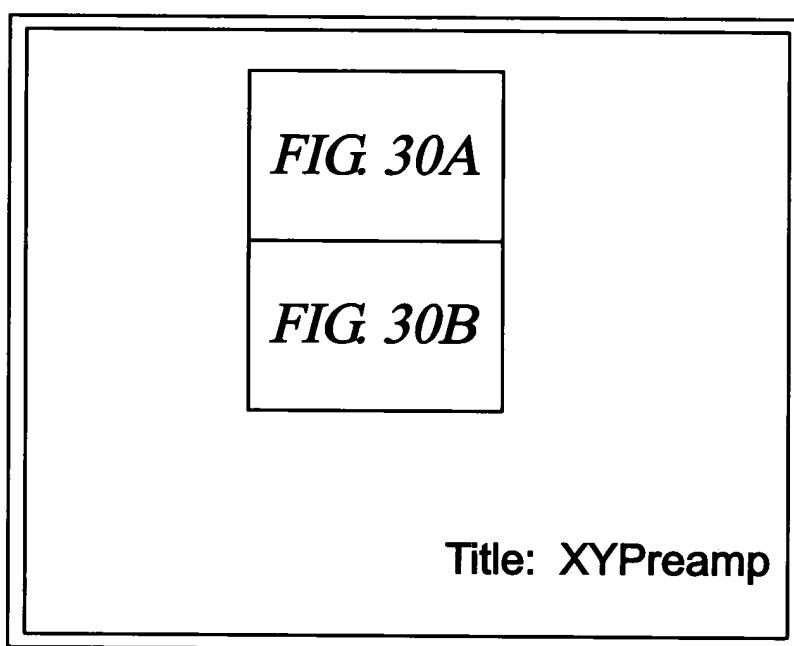


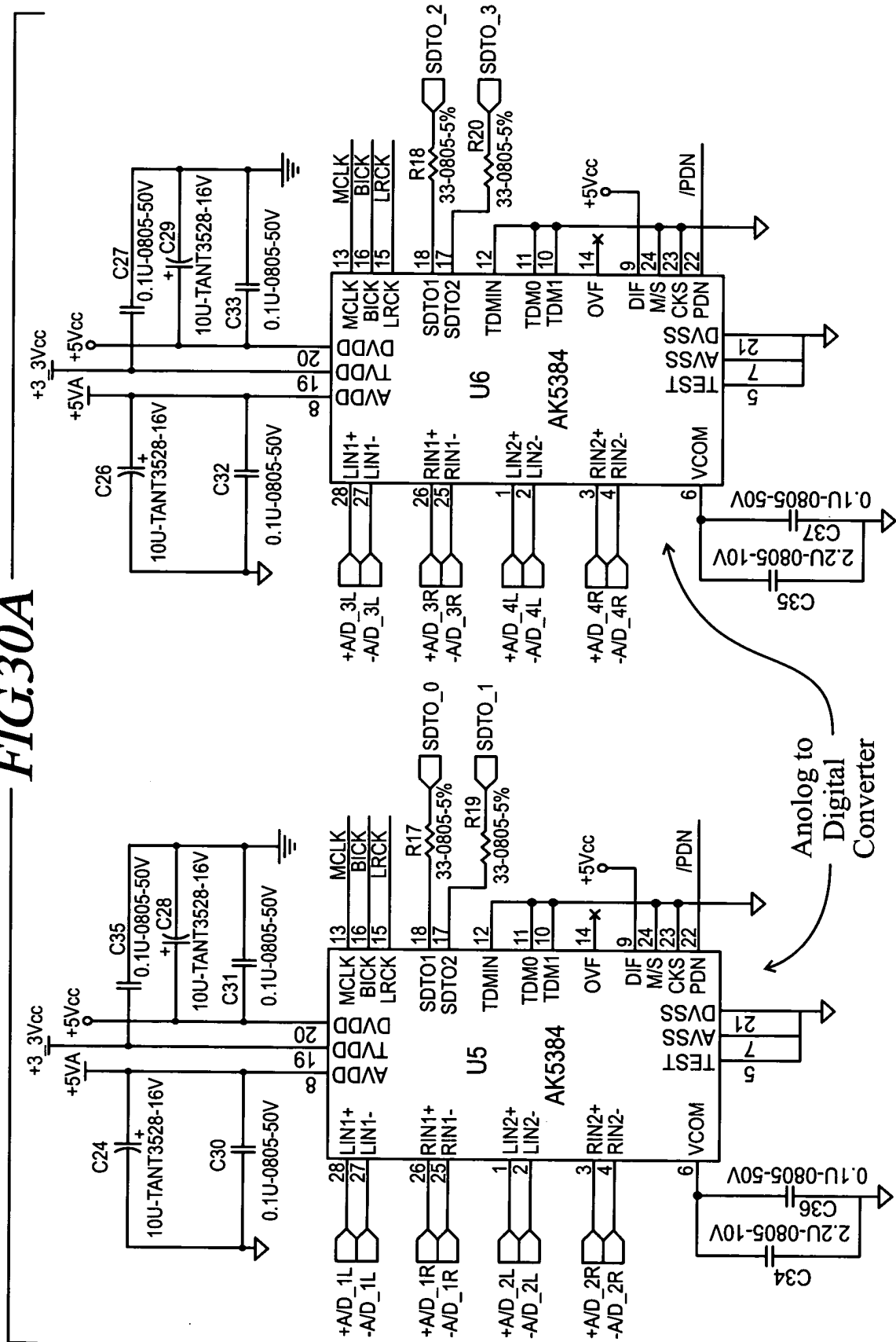
FIG. 29E

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*FIG. 30*

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**FIG.30A**

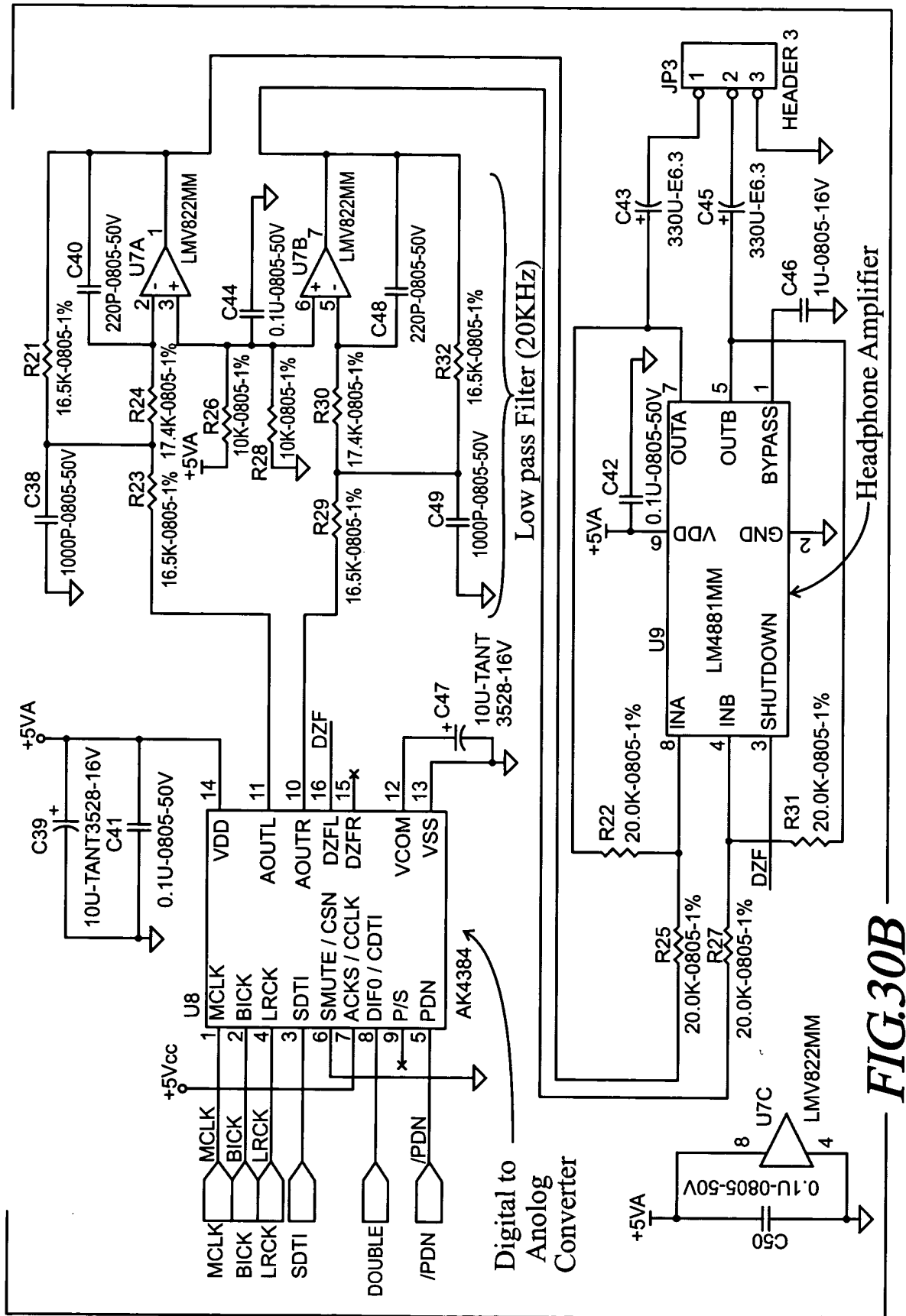
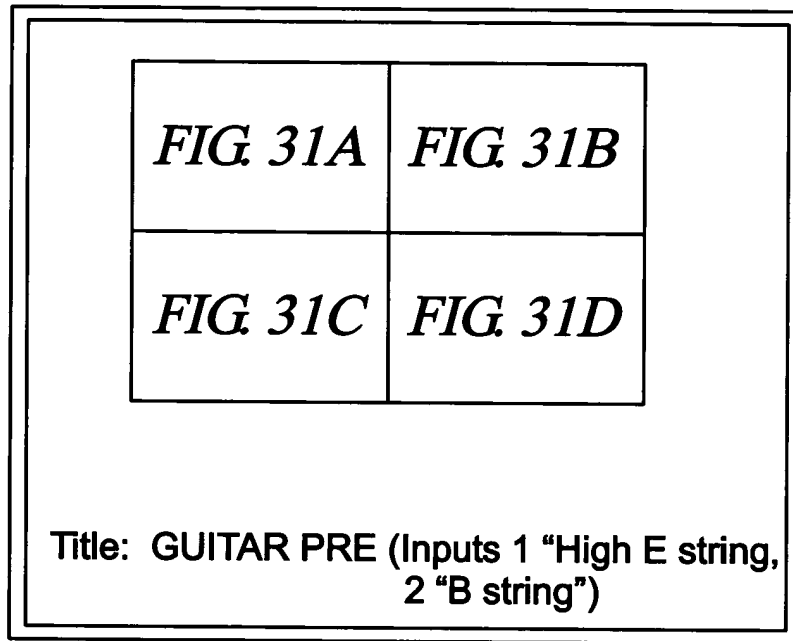


FIG.30B

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*FIG. 31*

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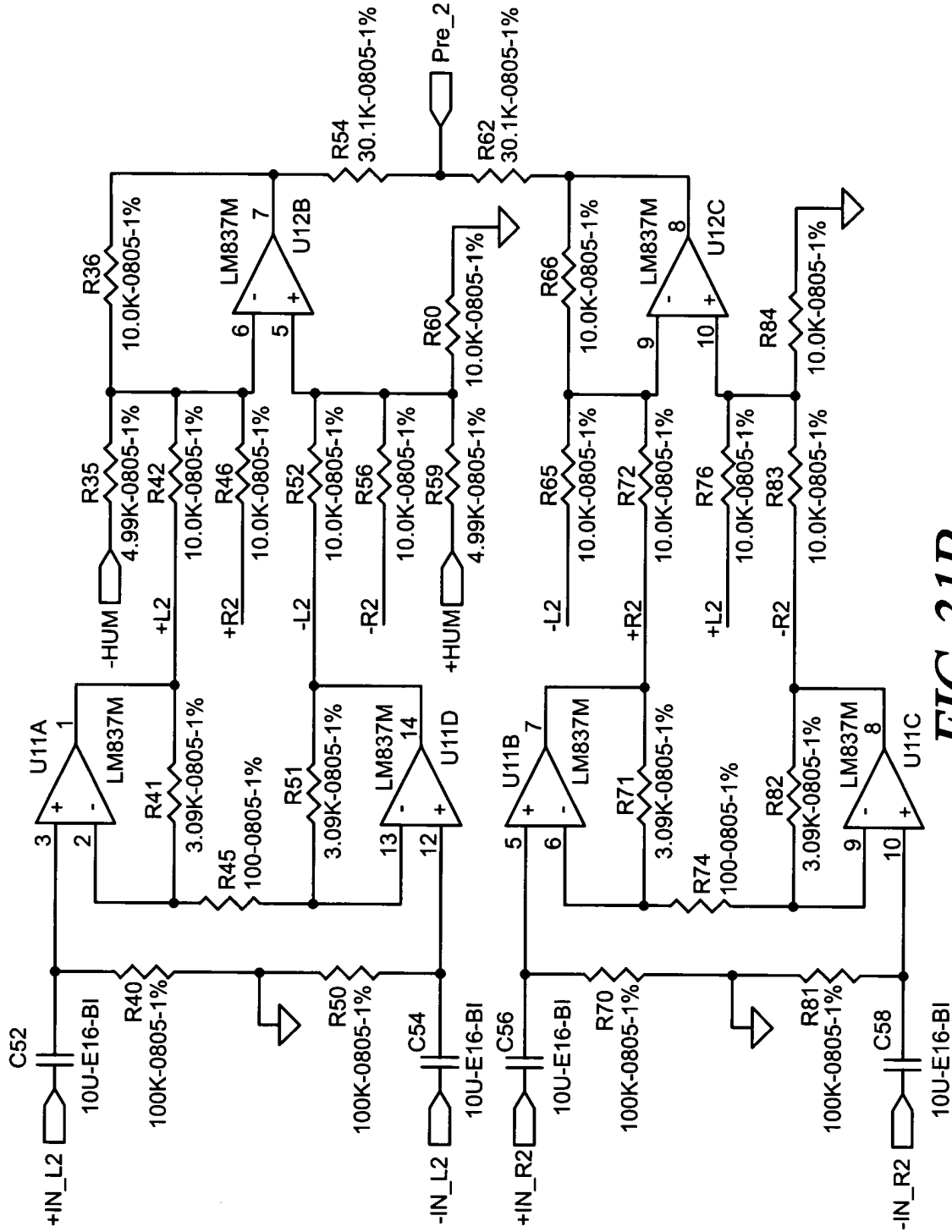


FIG. 31B

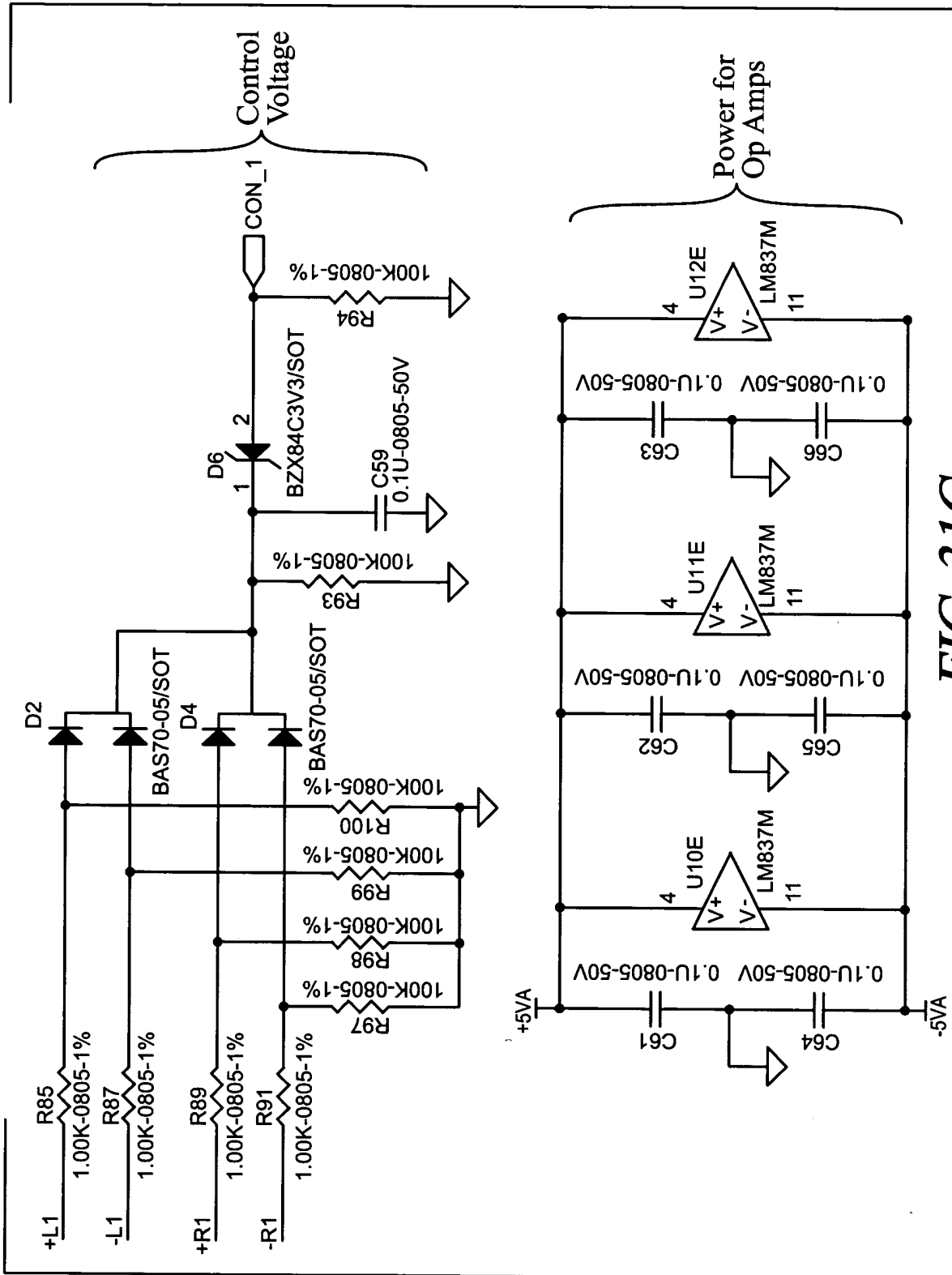


FIG 31C



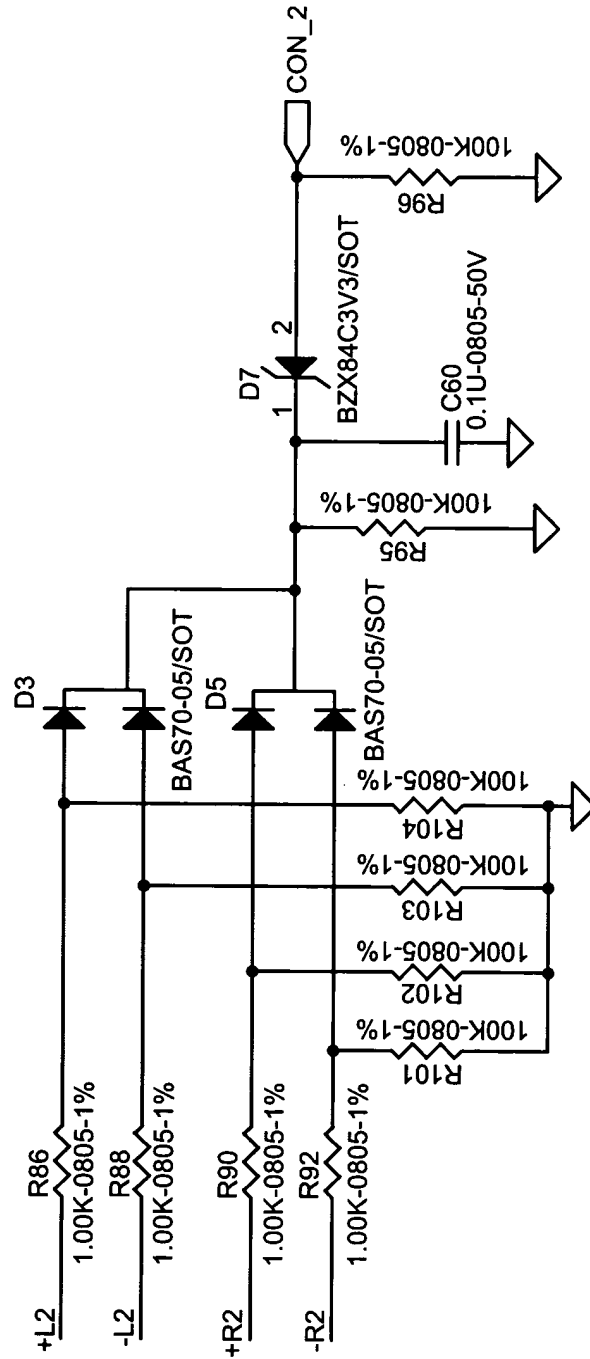
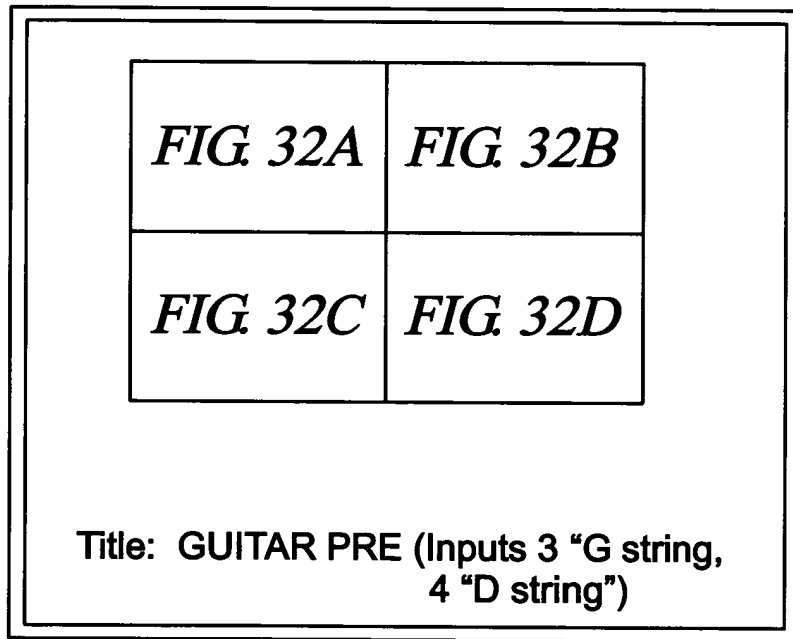


FIG 31D

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*FIG. 32*

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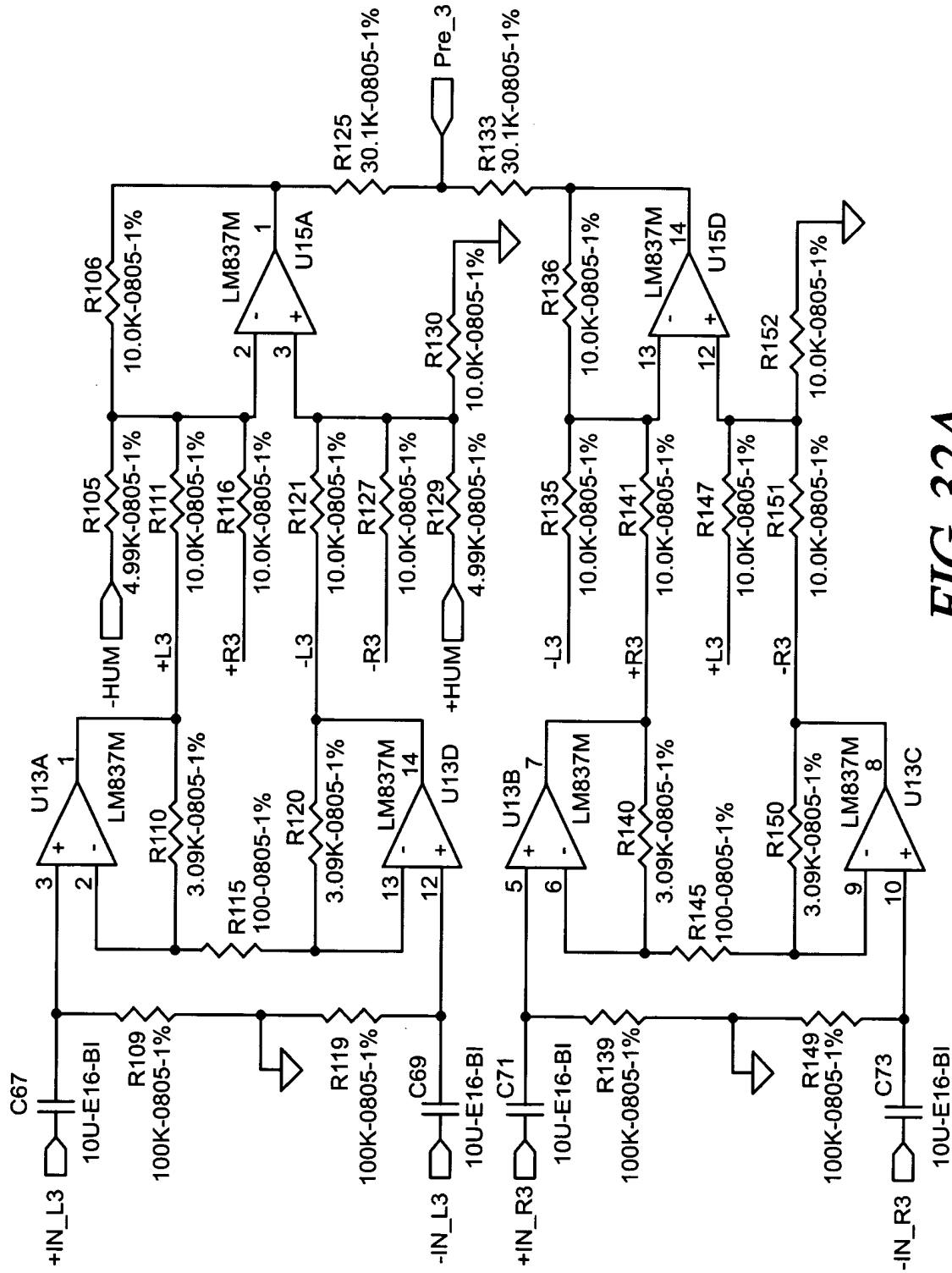


FIG. 32A

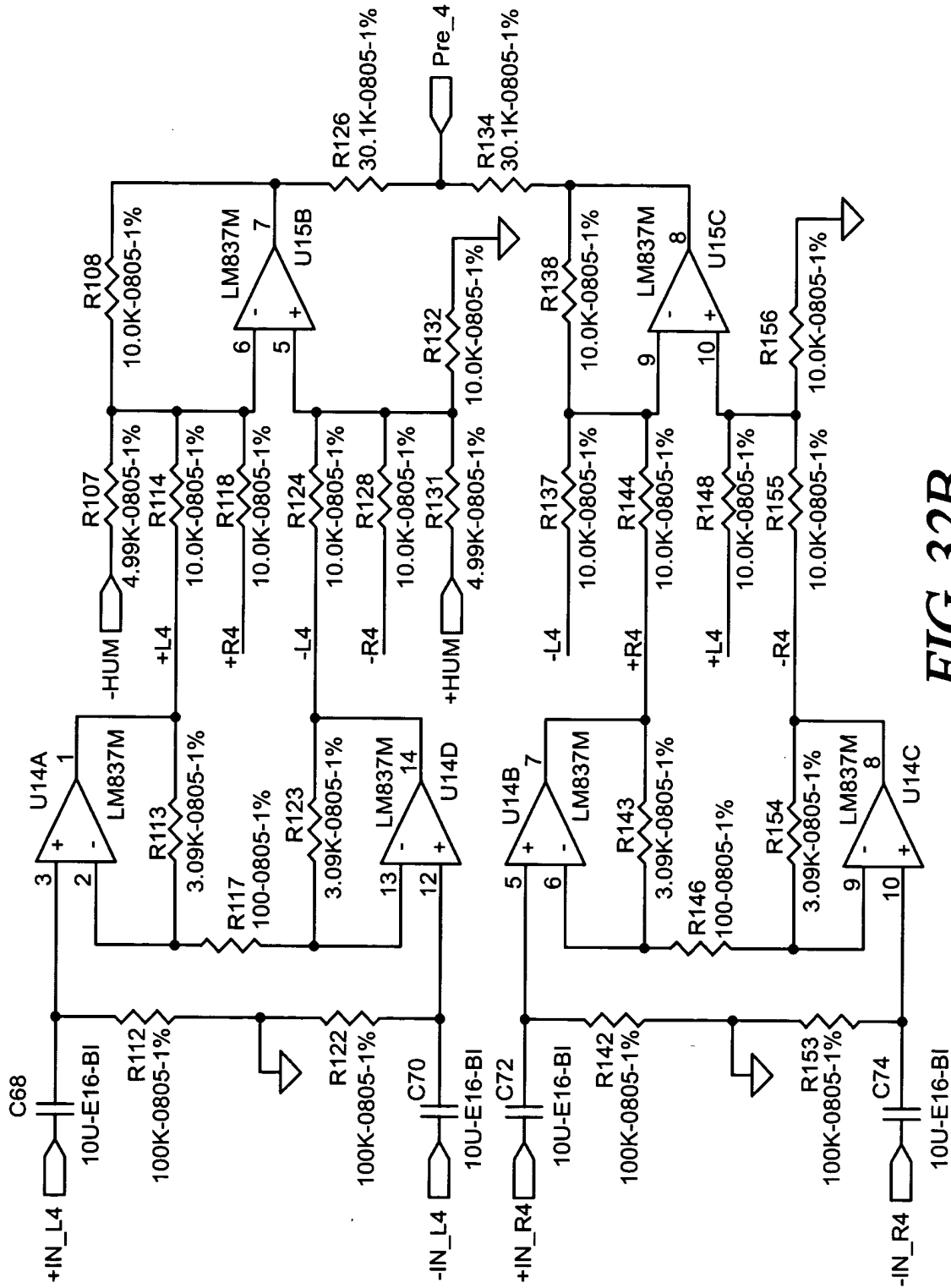


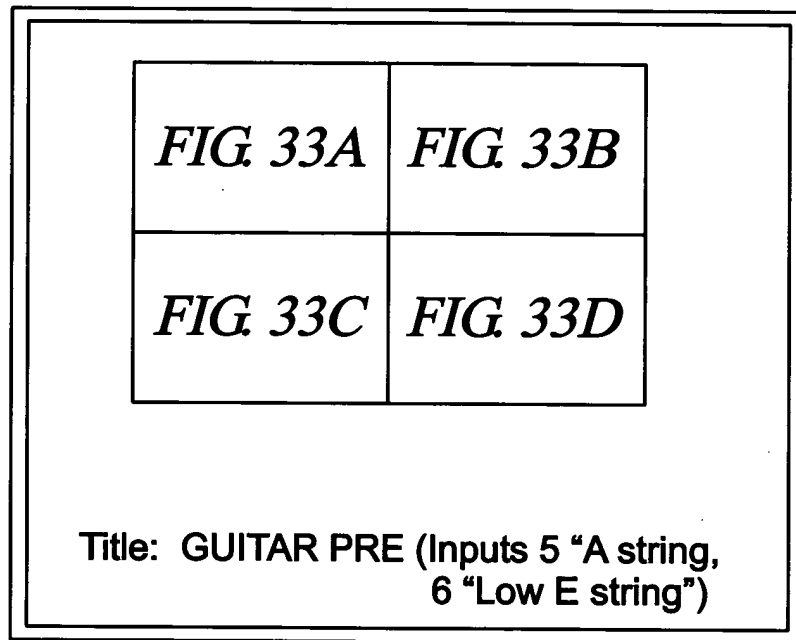
FIG. 32B





**FIG. 32D**

+



*FIG. 33*

+

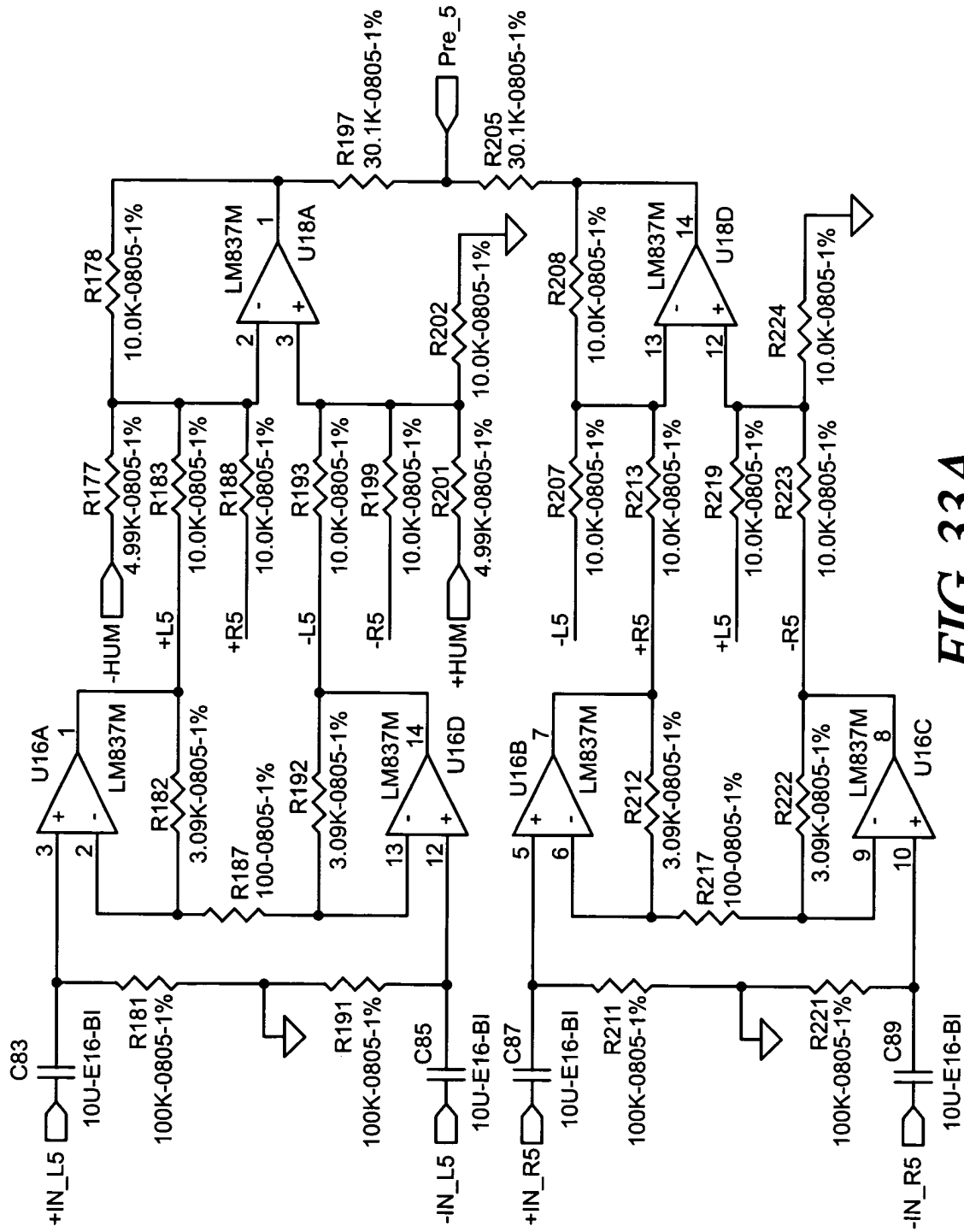


FIG. 33A



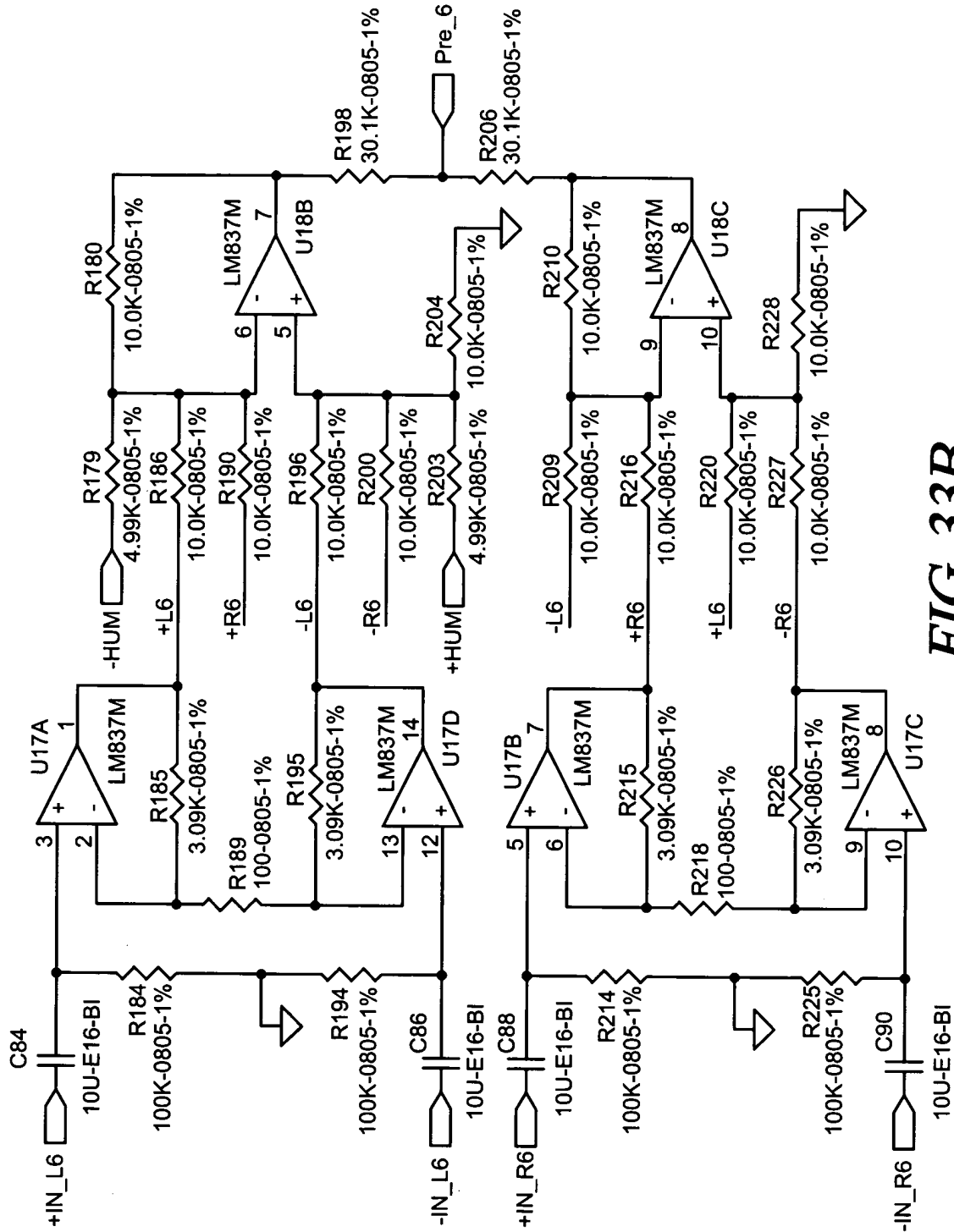


FIG. 33B

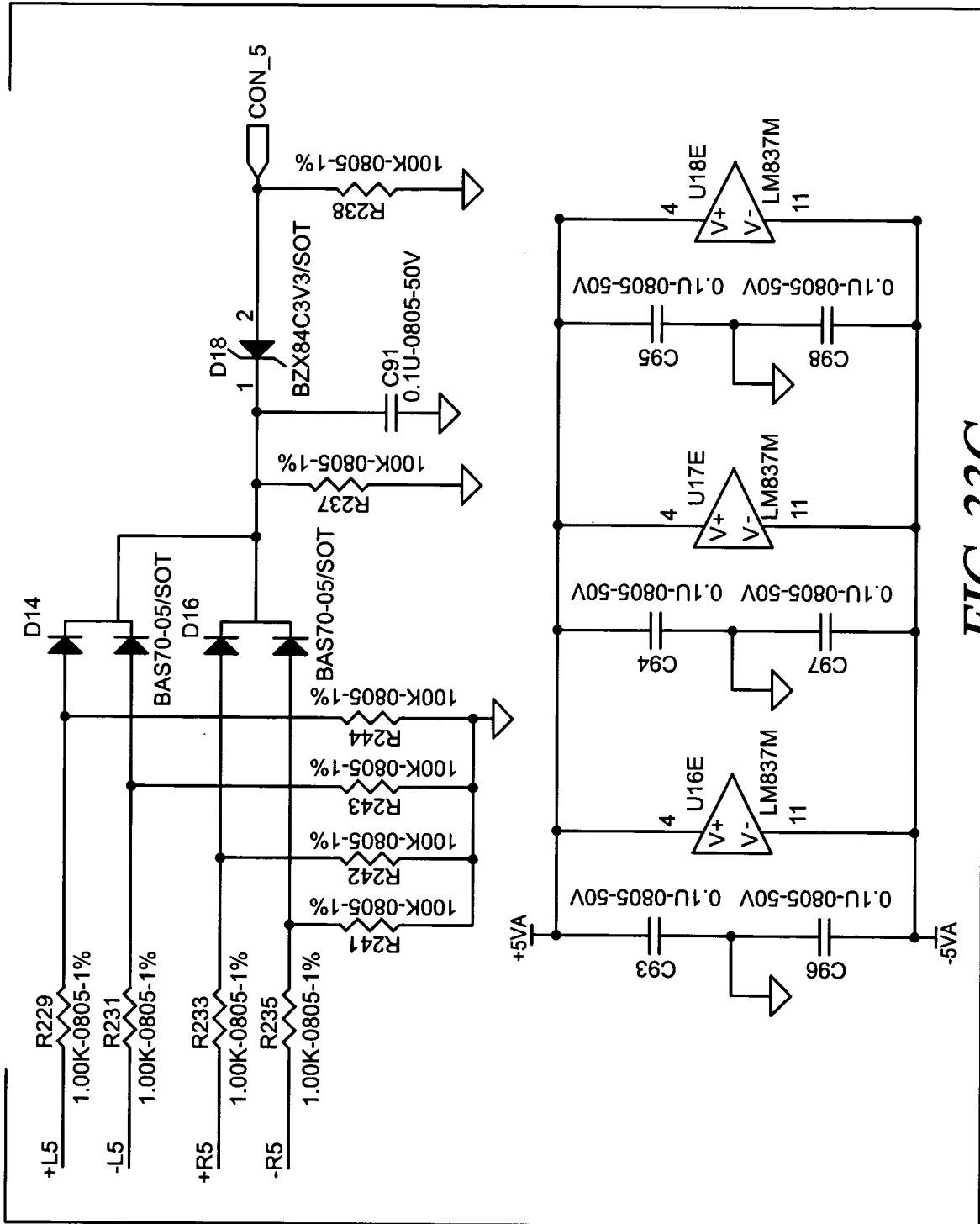
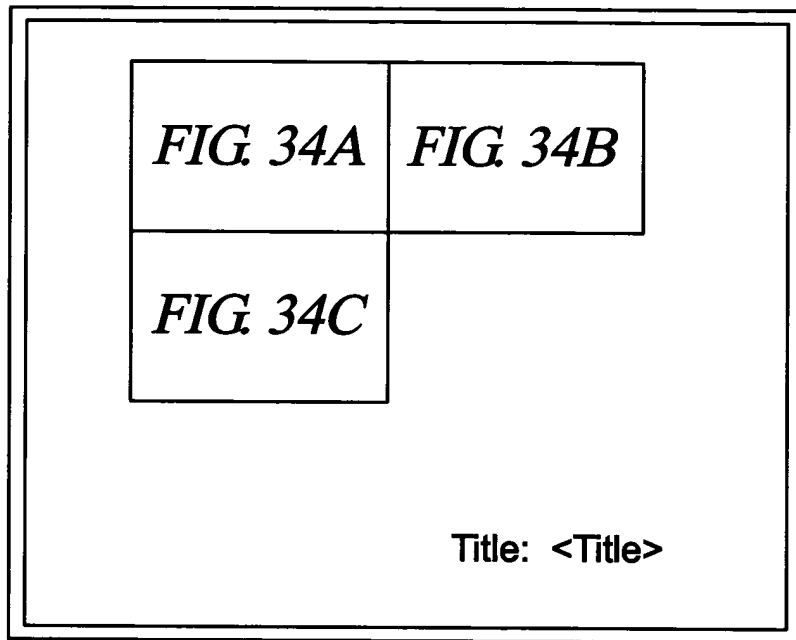


FIG 33C



**FIG. 33D**

+



*FIG. 34*

+

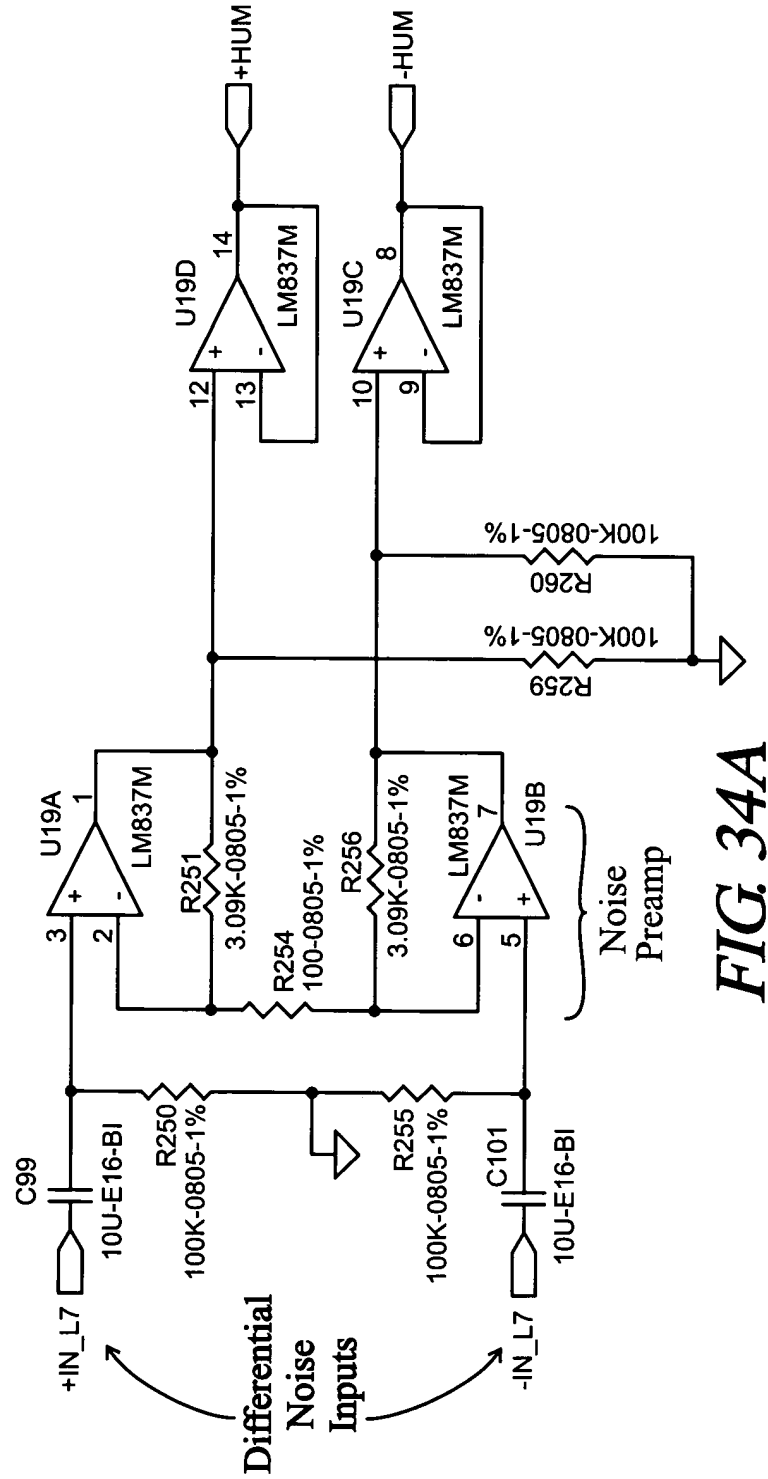


FIG. 34A

+

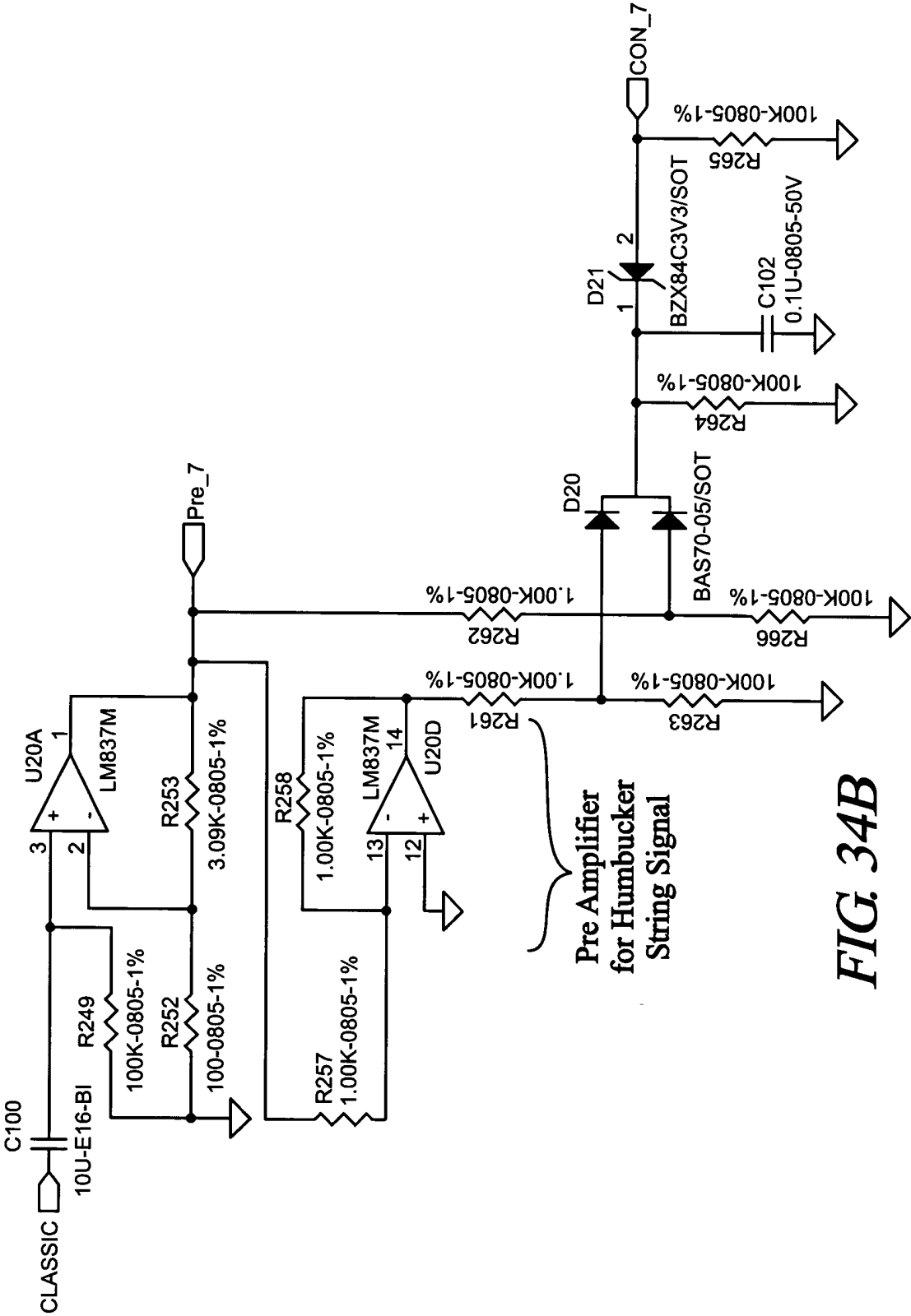
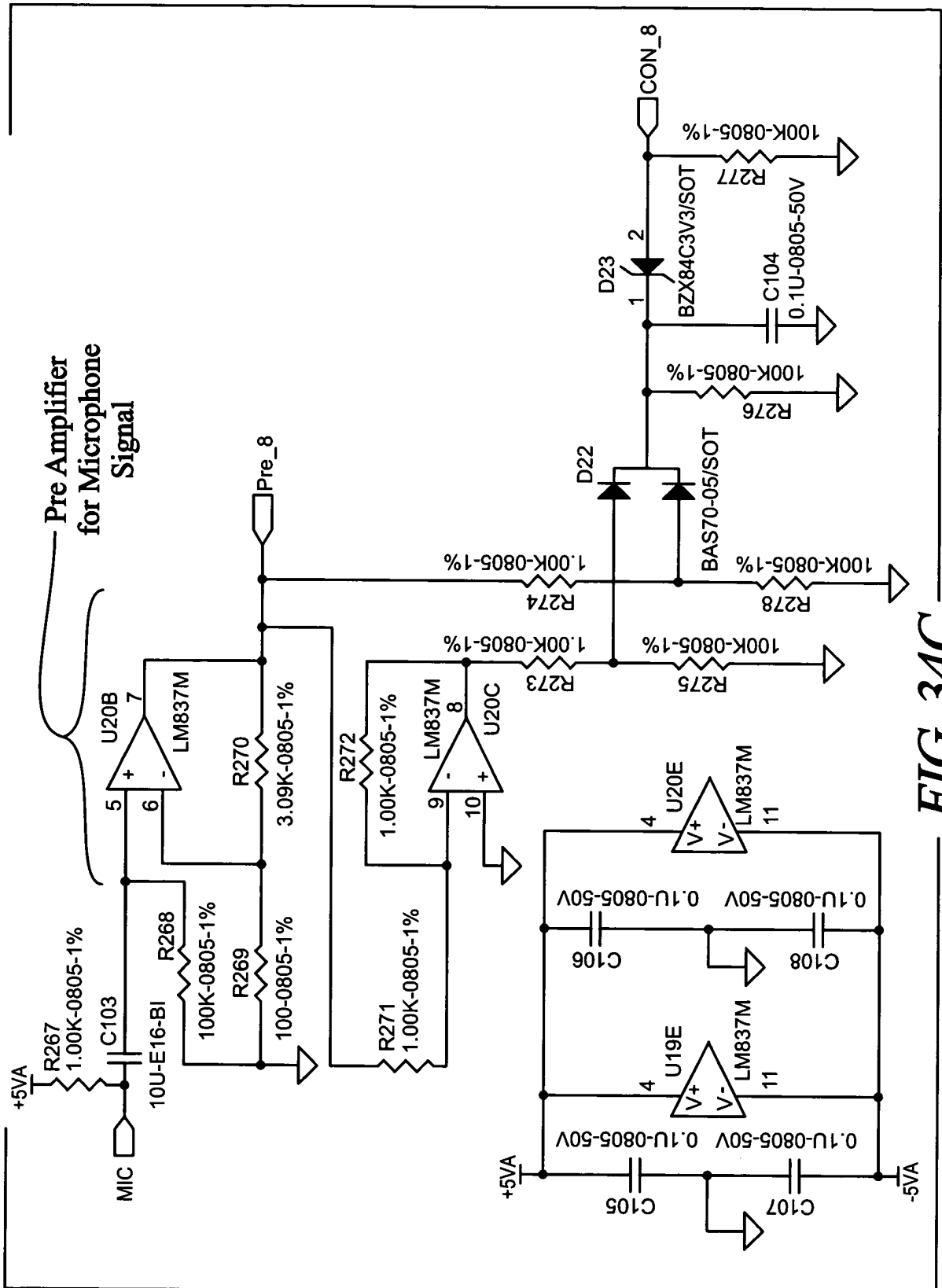


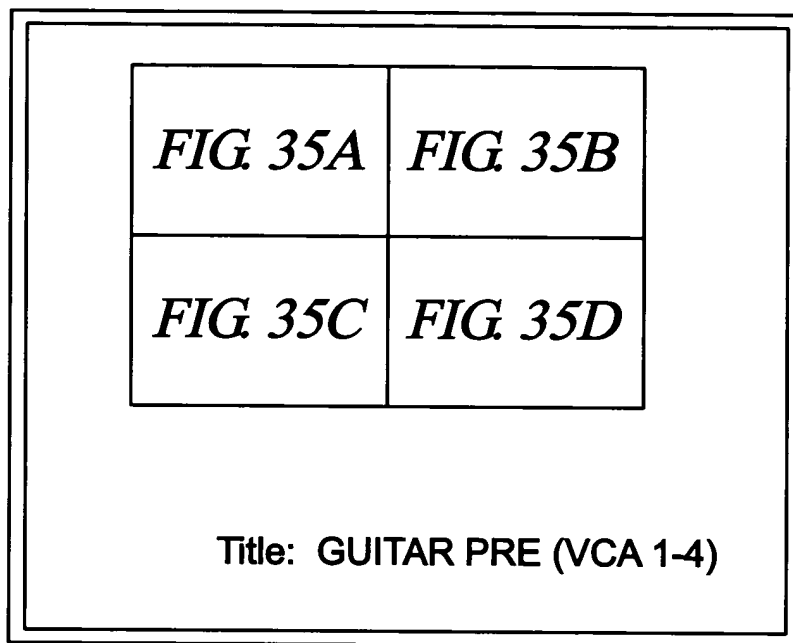
FIG. 34B

+



**FIG. 34C**

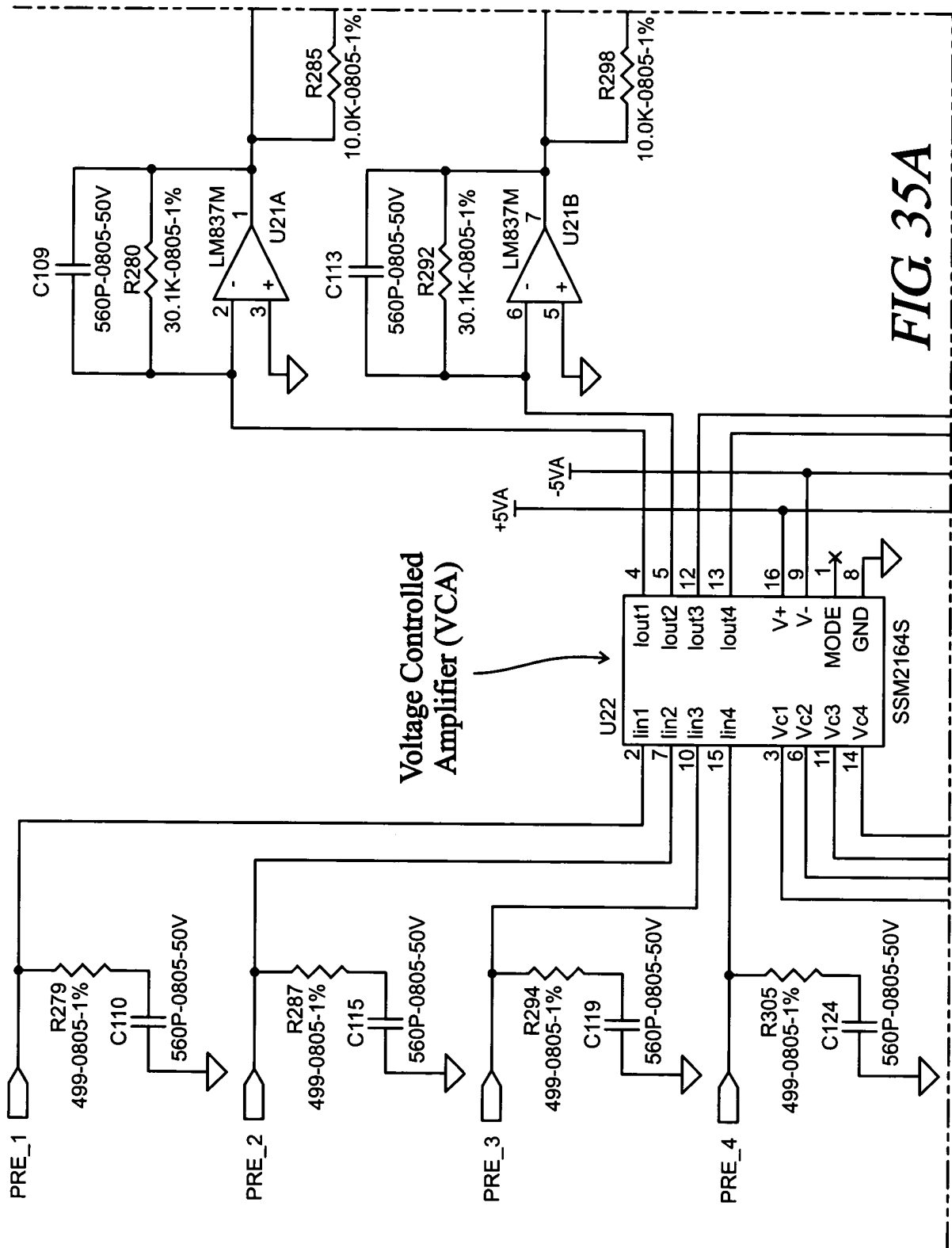
+



*FIG. 35*

+





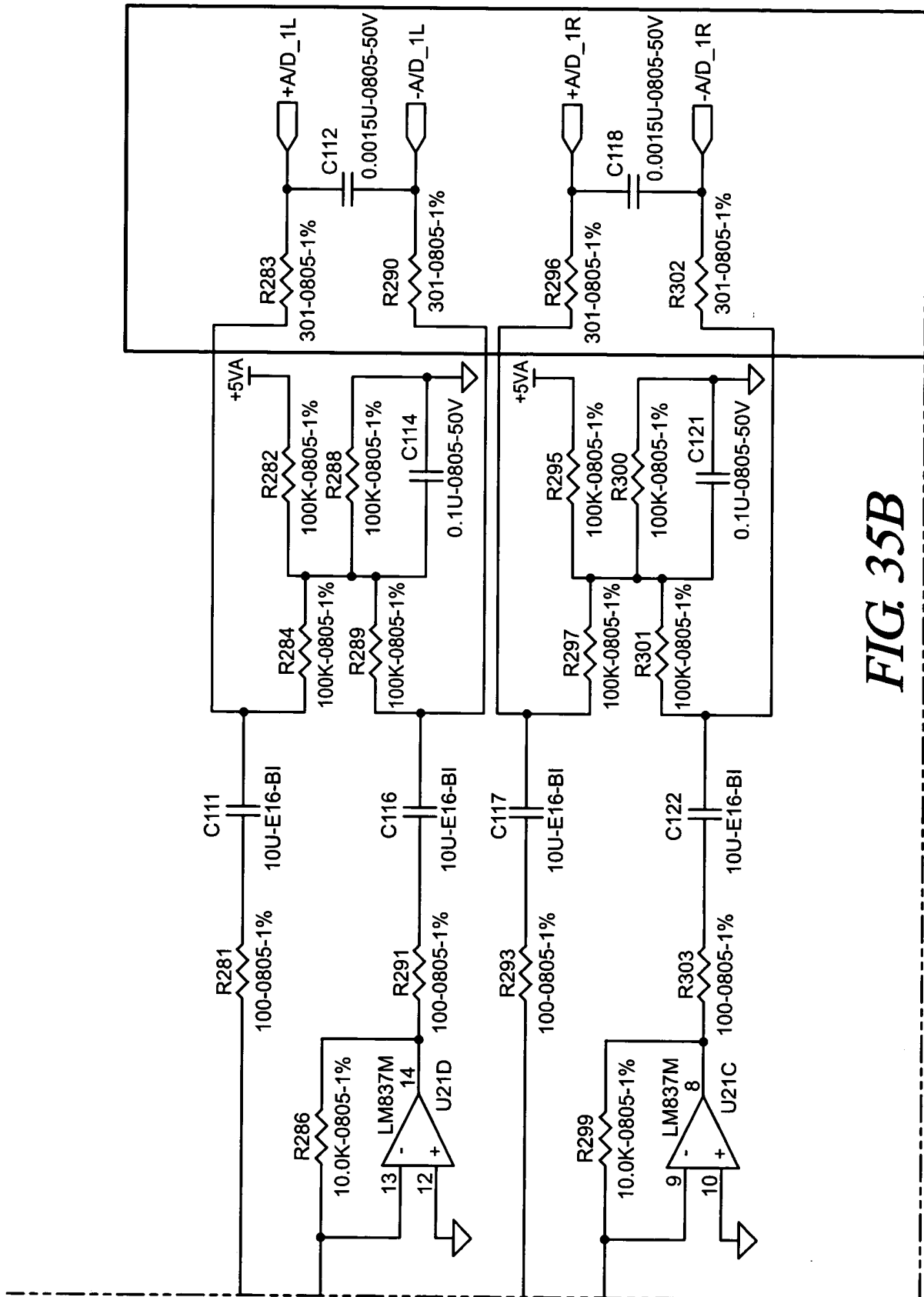


FIG. 35B

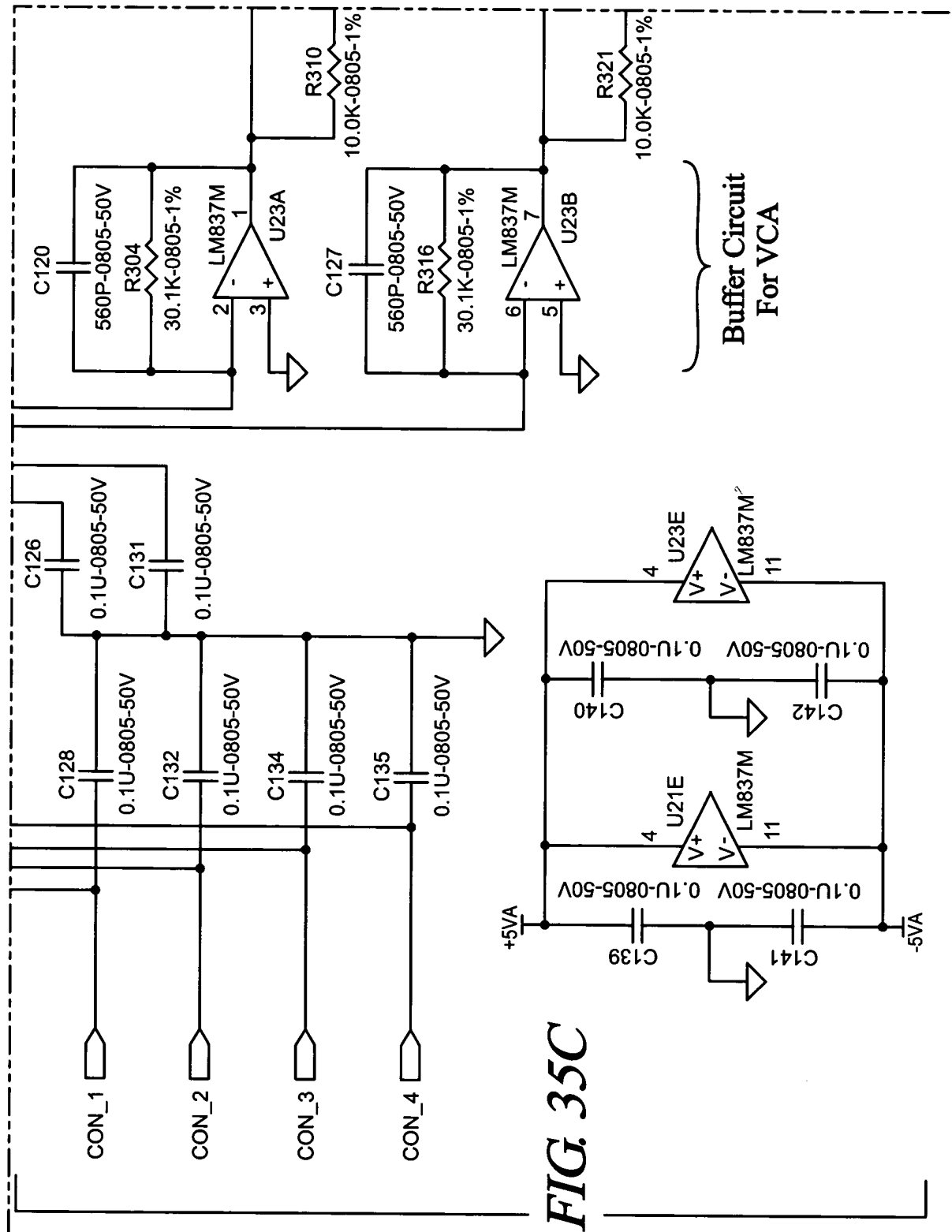
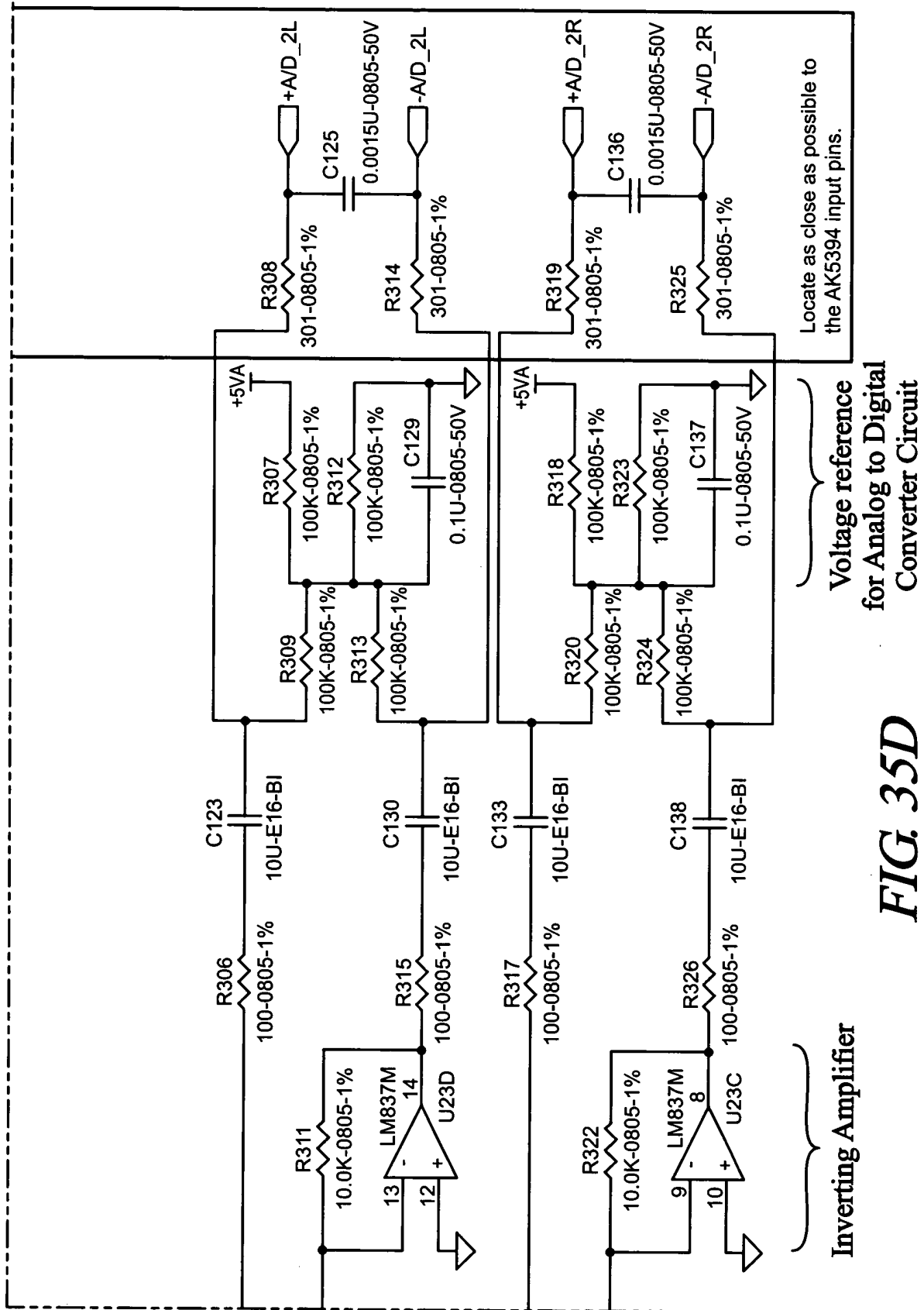


FIG. 35C

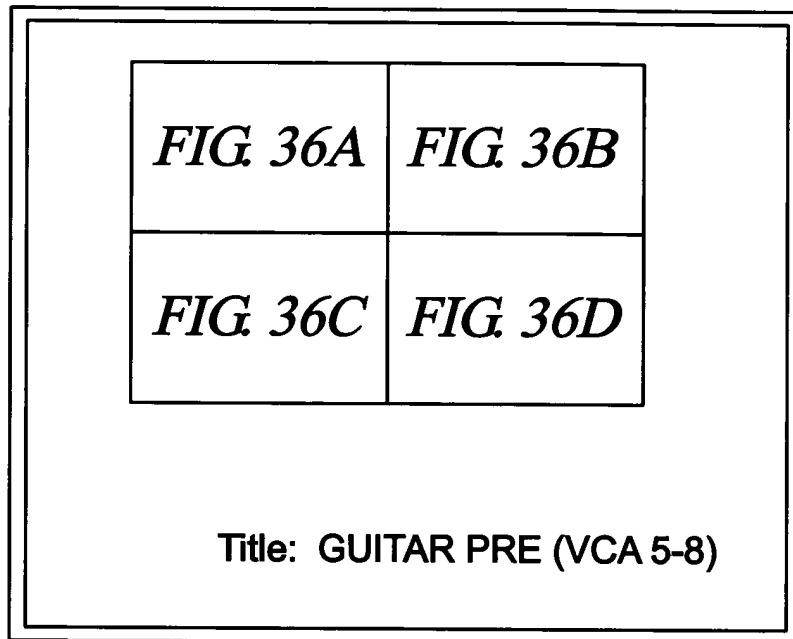


**FIG. 35D**

Inverting Amplifier

Voltage reference  
for Analog to Digital  
Converter Circuit

+



*FIG. 36*

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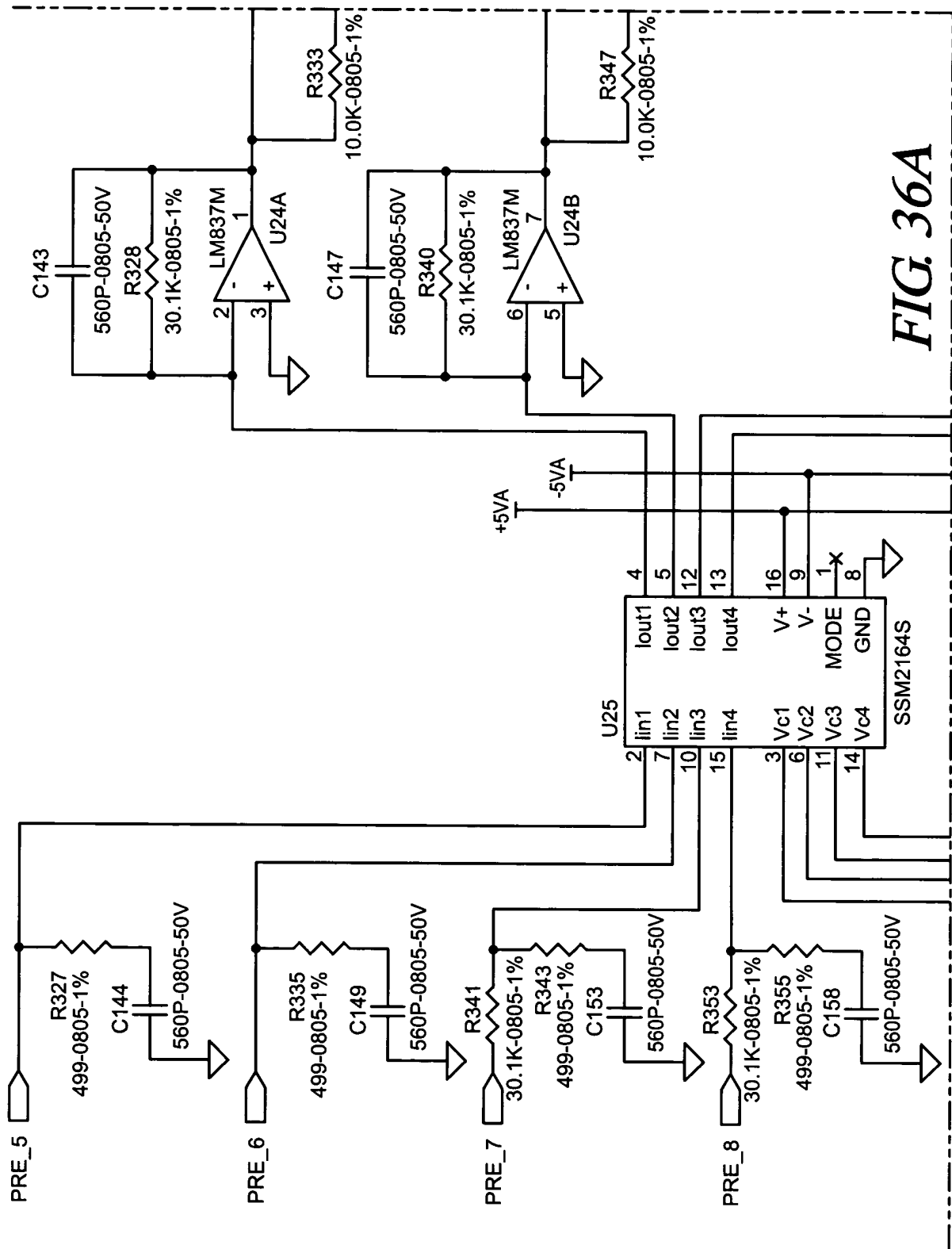


FIG. 36A

+

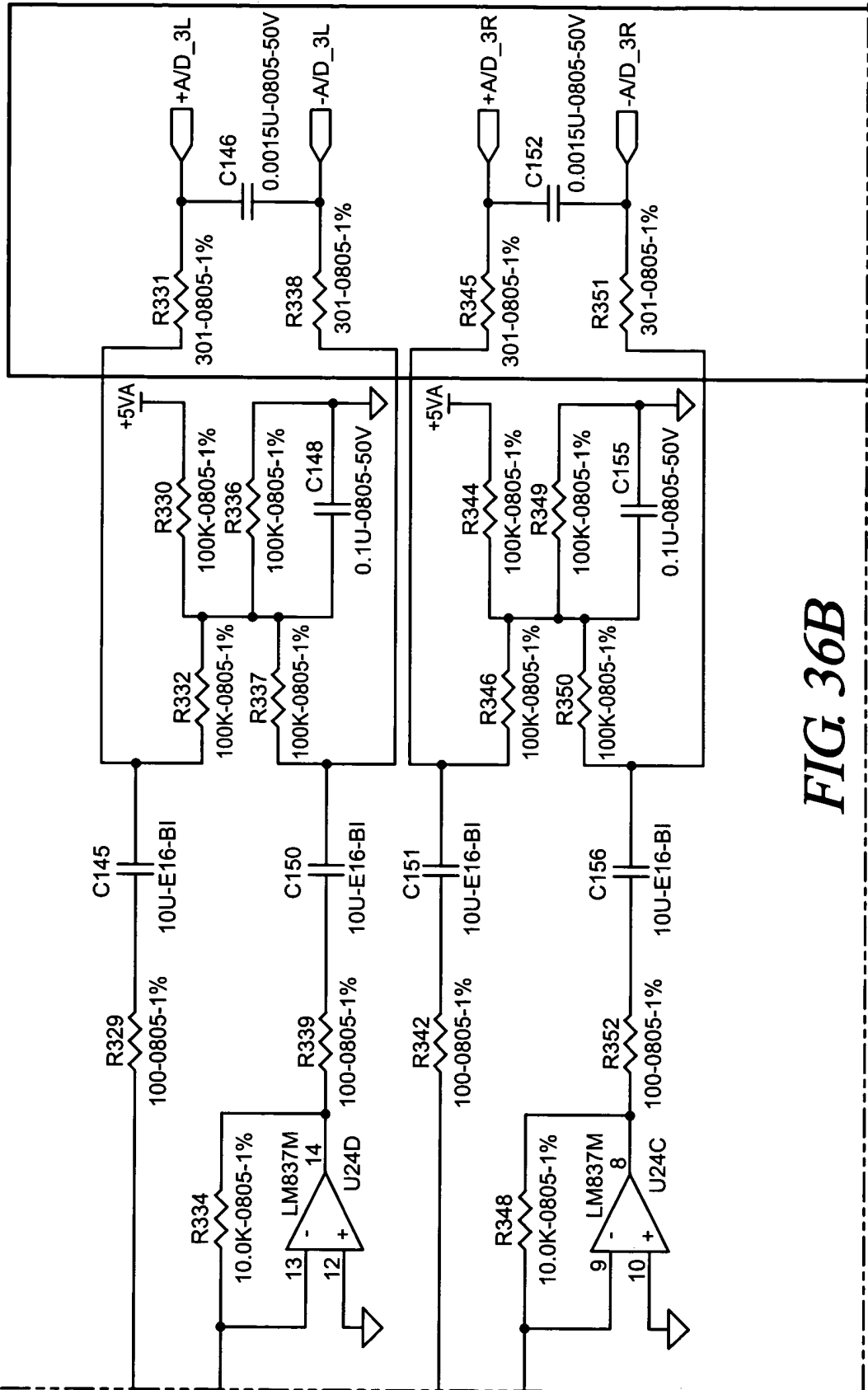


FIG. 36B

+

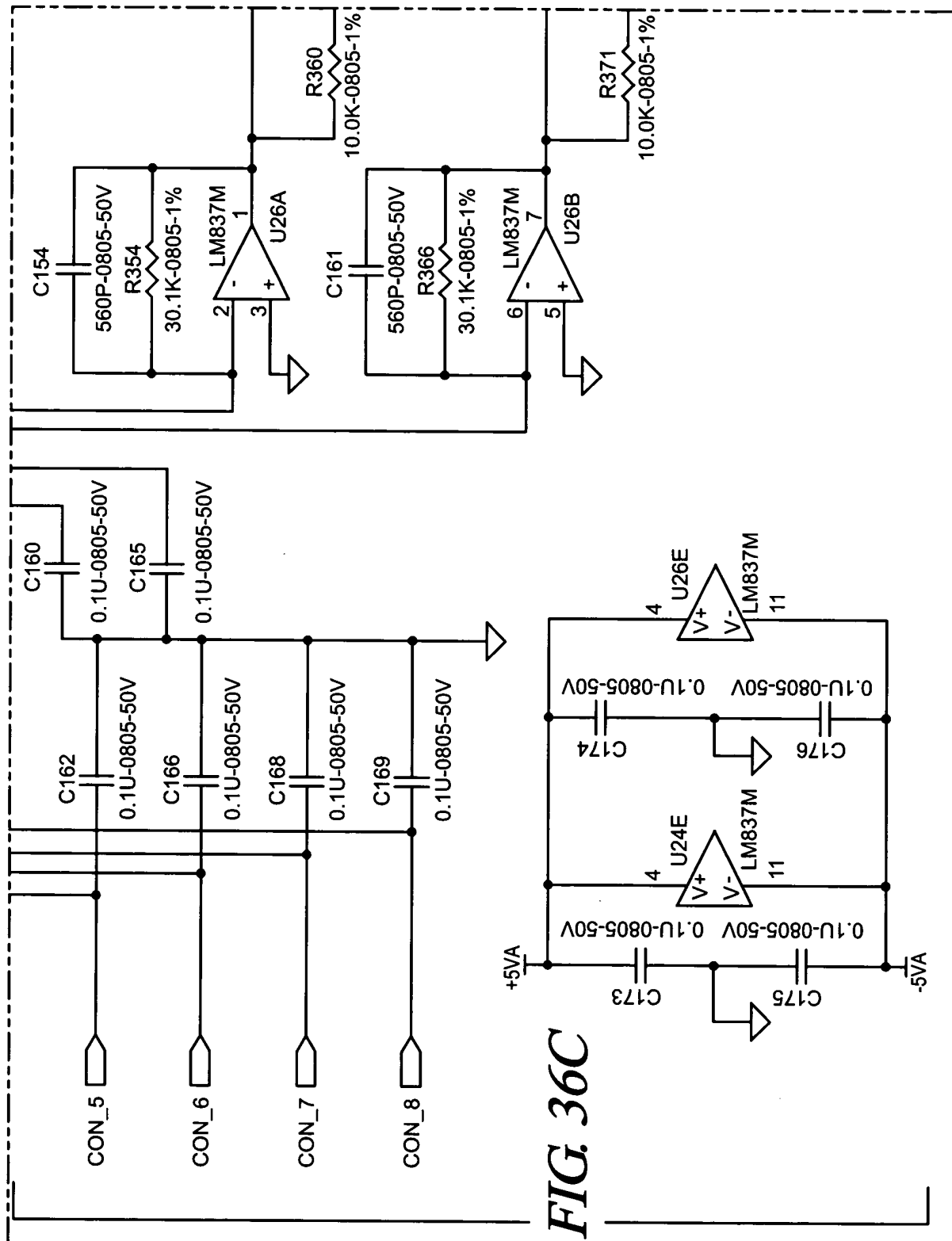


FIG. 36C





**FIG. 36D**